



OeS8
14.1 x 20.6 x 9.8 mm
SMD / DIL Metal Package

Features

- Pletronics' OeXO® Series Ovenized equivalent Temperature Compensated Crystal Oscillator
- Optional Voltage Control Function
- Low Power / Fast Warm Up
- CMOS Output
- 3.3V nominal Supply Voltage
- See tables for available Frequencies

Applications

SONET / SDH / DWDM
Test & Measurement
Telecom Transmission & Switching Equipment
Base Stations / Picocell
Wireless Communication Equipment

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition (Consult factory for other options)
Frequency Range ²	9.60	-	26.0	MHz	See table below for developed frequencies
Frequency Stability vs. Temperature ²	-50	-	+50	ppb	Over -40°C to +85°C at fixed V _{CC} + load (reference to midpoint min/max frequency) See factory for other options
Frequency Initial Calibration		-	±1.0	ppm	V _{control} 1.50 volts at 25°C ± 2°C when V _{CC} ≥ 2.8 volts If V _{control} used
Operating Temperature Range ²	-40	-	+85	°C	
Supply Voltage ^{1,2} V _{CC}	-	3.3	-	Volts	± 5%
Supply Current ² I _{CC}	-	20	30	mA	10 MHz 20 MHz Load: 15 pF, V _{CC} ± 5%
Frequency Stability vs. Supply	-	-	±0.05	ppm	Load: 15 pF, V _{CC} ± 5%
Frequency Stability vs. Load	-	-	±0.05	ppm	Load: 15 pF ± 10%
V _{control} Range	0.5	-	2.5	Volts	1.50 volts nominal for V _{CC}
Frequency Pullability ²	0	±6.0	±8.0	ppm	Positive Slope
Linearity	-	-	2.0	%	
Output Waveform	CMOS				
Duty Cycle	40	50	60	%	Load: 15 pF
Output V _{HIGH}	90	-	-	%V _{DD}	V _{th} : T _R and T _F 10% and 90% of amplitude V _{th} : D.C. 50% of amplitude
Output V _{LOW}	-	-	10	%V _{DD}	
Output T _{RISE} and T _{FALL}	-	-	6.5	nS	
Startup Time	-	25.0	-	mS	Within ± 200 ppb of final frequency
Long Term Stability (Aging)	-	-	±2.0	ppm	10 years at 25°C ± 2°C
Phase Noise	100 Hz 1 kHz 10 kHz 100 kHz	-	-120 -134 -144 -145	-	dBc/Hz 25°C ± 2°C at 20.0 MHz
Storage Temperature Range	-55	-	+90	°C	

Note: ¹ Place a 10nF power supply bypass capacitor next to device for correct operation
² Typical capabilities shown. A unique OeXO® datasheet is created for each specific device. See Factory for other options.

The following is a list of developed frequencies. Consult factory for other options.

9.60M, 10.00M, 13.00M, 16.384M, 19.20M, 20.00M, 26.00M only

Part Number (Possible Options shown)

Series Model	V _{CC} Supply Voltage ¹	Operating Temperature		Stability ^{1,2}	Pullability ¹	Frequency
		Lowest	Highest	(ppm)	(ppm)	(MHz)
OES8	A unique number will be assigned for your exact specification					-19.20M
	3.3 volts nominal	-40°C	+85°C	± 0.05	0 ± 5 ± 8	9.6 - 26 MHz Developed: 9.60M, 10.00M, 13.00M, 19.20M, 20.00M, 26.00M

¹ Contact Factory for non-standard specifications

² Not all stabilities are available with all operating temperature ranges. Contact Factory for exact combinations available.

Device Marking

PLE OES8* FFFFM • YMDzzz	PLE = Pletronics FFFF = Frequency in MHz YMD = Date code zzz = Internal factory codes
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* A unique number is assigned for your exact specifications.

Specifications such as part number, frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	6	7	8	9	0	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2016	2017	2018	2019	2020	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31

Package Labeling

Tube or pad packaging is available.

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
 Font is Courier New
 Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)
 Font is Arial

P/N:  OES8001-20.0M Customer P/N:  12345678 Qty:  1000 D/C  MSL: 1 OeXO® 503-M8ZU4

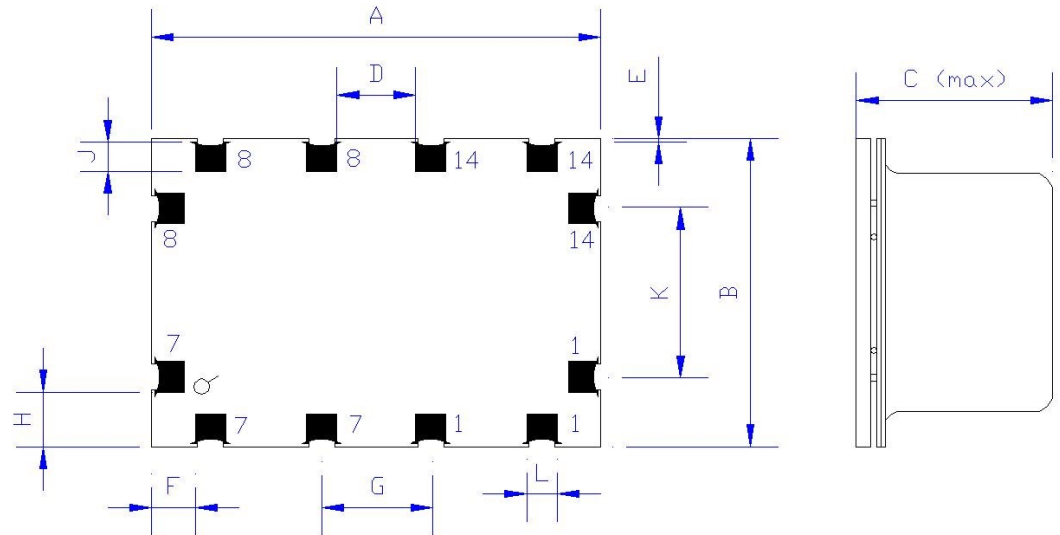
RoHS Compliant 2nd Lvl Interconnect Category=e4 Max Safe Temp=245C for 10s (Reflow Only) 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS 2 (2011/65/EU) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
 Weight of the Device: 4.0 grams
 Moisture Sensitivity Level: 1 As defined in J-STD-020D
 Second Level Interconnect code: e4

Mechanical Dimensions

	Inches	mm
A	0.811 ± 0.004	20.60 ± 0.1
B	0.555 ± 0.004	14.10 ± 0.1
C	0.386 max	9.80 max
D ¹	0.147	3.73
E ¹	0.006	0.15
F ¹	0.079	2.00
G ¹	0.200	5.08
H ¹	0.098	2.5
J ¹	0.053	1.35
K ¹	0.301	7.65
L ¹	0.053	1.35



¹ Typical dimensions

(Not to Scale)

Cover: Kovar, Electroless Nickel Plated 1µinch (25µm) typical, Resistance welded to base, Laser Marked
Base: Kovar, Glass to metal sealed leads
PCB: FR4

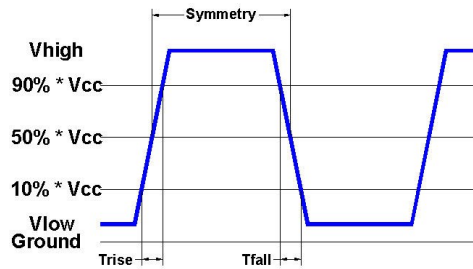
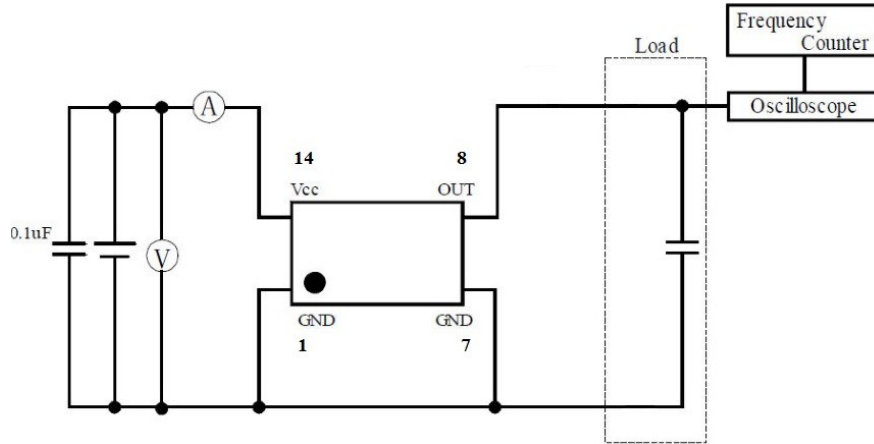
Layout

Pin	Function	Note (Solder pad layout may use any combination of pins 1, 7, 8 & 14 shown)
1	Vcontrol Input	If this function is not specified, recommend connecting this pin to ground. EFC (Electronic Frequency Control).
7	Ground (GND)	
8	Output	CMOS
14	V _{cc} Supply Voltage	Connect an appropriate 10nF power supply bypass capacitor as close as possible

For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans
- Minimize air flow across the device

Electrical Test /Load Circuit



Environmental / ESD Ratings

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	JESD22-B104
Vibration	JESD22-B103
Solderability	IPC J-STD-002
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Charged Device Model	500V	JESD 22-C101
Machine Model	200V	JESD22-A115

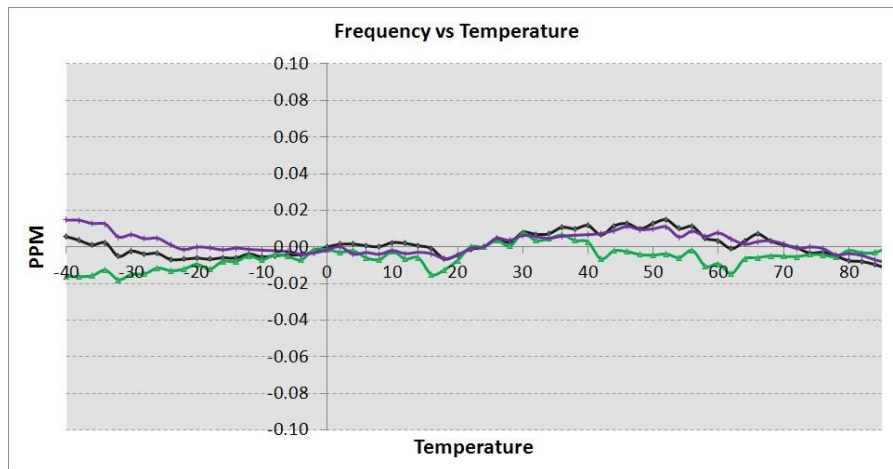
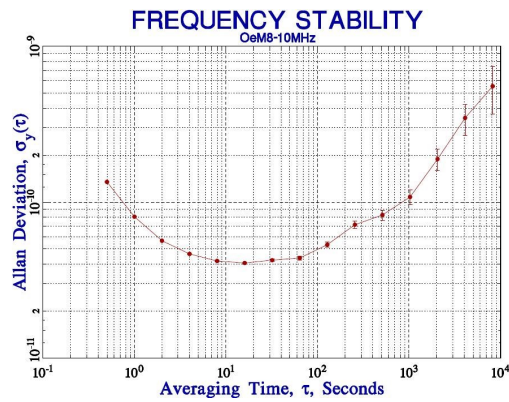
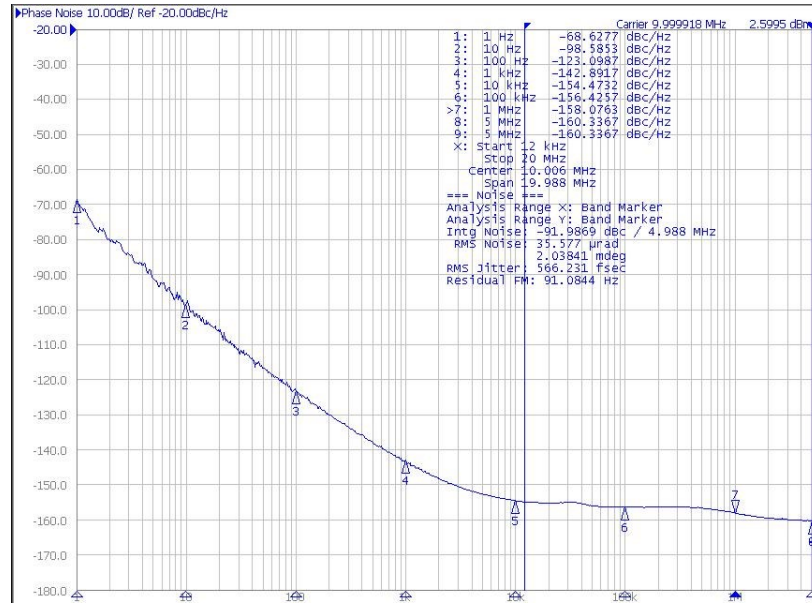
Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.6V to +6.0V
V _i Input Voltage	-0.6V to V _{CC} + 0.6V
I _o Output Current	-10mA to +10mA

Thermal Characteristics:

The maximum die or junction temperature is 155°C
The thermal resistance junction to board is 120°C/Watt depending on the solder pads, ground plane and construction of the PCB.

Charts



Important Notice

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