

Pletronics

Voltage Controlled Crystal Oscillators

VCXO

RoHS 6/6 Compliant

VHD6 CMOS Output

LCC Ceramic Package
3.2 x 5.0 x 1.35 mm

- ◆ Fundamental Mode crystal
- ◆ Enable/Disable function on pad 2
- ◆ Small SMD package for high density board designs



VHA6 CMOS Output

LCC Ceramic Package
5.0 x 7.0 x 1.7 mm

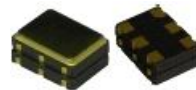
- ◆ Fundamental Mode Crystal
- ◆ Enable/Disable function on pad 2



VLU7 LVDS Output

LCC Ceramic Package
5.0 x 7.0 x 2.97 mm

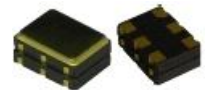
- ◆ Output frequency is synthesized from fundamental quartz crystal
- ◆ Enable/Disable function on pad 2
- ◆ +/- 50 PPM APR



VPU7 LVPECL Output

LCC Ceramic Package
5.0 x 7.0 x 2.97 mm

- ◆ Output frequency is synthesized from fundamental quartz crystal
- ◆ Enable/Disable function on pad 2
- ◆ +/- 50 PPM APR



FEATURES

- ◆ Excellent phase noise characteristics
- ◆ All have Tri-State with Power Standby modes
- ◆ Temp range: -10° to +70°C Standard, -40° to +85°C Extended
- ◆ Available Tolerances: +/- 20PPM, +/- 25PPM, and +/- 50PPM
- ◆ Most common frequencies in stock

APPLICATIONS

- ◆ Ideal reference clocks for high-speed applications requiring low jitter, including:
 - ◆ 1/10 Gigabit Ethernet
 - ◆ 2/4/10G Fibre Channel
 - ◆ Server & Storage platforms
 - ◆ SONET/SDH linecards.

Pletronics Part Series	Package Size	Frequency Range (MHz)	Tr/Tf Maximum (nS)	Duty Cycle %	RMS Jitter (pS)		Current with Load (ma)	Supply Voltage		Supply Noise Rejection
					12KHz-20MHz	10Hz-20MHz		2.5V	3.3V	
VHD6	3.2x5 mm	10-108	6.0	45-55	0.6		8.0	*	*	Good
VHA6	5x7 mm	1.7-108	6.0	45-55	0.6		8.0	*	*	Good
VLU7	5x7 mm	10.9-670	0.3	47-53	0.6	3.2	90		*	Good
VPU7	5x7 mm	10.9-766 876-1,170	0.3	47-53	0.8	3.2	110		*	Good

◆ See individual data sheets and standard part number builder on www.pletronics.com for allowable configurations and complete specifications

Standard Part Number Generation

