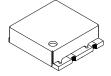


# **SM7645H CMOS Series**

- CMOS with Enable/ Disable or Optional Stand By Mode (3.3 V)
- Fundamental or 3rd Overtone Crystal Used
- 4 Pad Leadless Surface Mount Clock Oscillator



1.500 MHz - 69.999 MHz

### **Standard Specifications**

Overall Frequency Stability
Operating Temperature Range
Supply Voltage (Vcc)
Symmetry (Duty Cycle)

0 to +70°C is standard, but can be extended to - 40 to +85°C for certain frequencies 5.0 volts, 3.3 volts, 2.5 volts and 1.8 volts available, .01 µF bypass cap recommended 40/60 to 60/40% is standard, but 45/55% at 50% of Vcc is also available (see Waveform 1)

SM7645H: ± 50 PPM, SM7644H: ± 25 PPM, SM7620H: ± 20 PPM over Operating Temp. Range

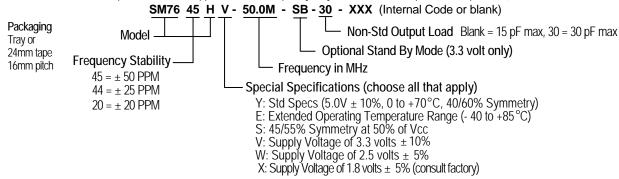
Logic Levels Logic "1" 90% of Vcc MIN; Logic "0" 10% of Vcc MAX

Output Load Standard load is 15 pF (typ. 1 ASIC) maximum, see Test Circuit 2 (consult factory for heavier loads)
Enable/Disable Option (E/D) Output enabled when Pin #1 is open or at Logic "1"; Output disabled when Pin #1 is at Logic "0".

Frequency Range (MHz)		Max. Supply Current Icc (mA) w/ 15pF load		Max. Rise and Fall Time Tr & Tf (nS) w/ 15pF load			M T 0 T( / C)
	1.500 - 10.999	7	10	5.0	Range (MHz) 1.500 - 39.999 10.000 - 69.999	w/ 15pF load	w/ 15pF load
	11.000 - 23.999	15	15	5.0		1.8V	1.8V
	24.000 - 29.999	15	20	5.0		10	5.0
	30.000 - 45.999	20	30	5.0		25	3.0
	46.000 - 69.999	25	45	4.5			

#### **Part Numbering Guide**

Portions of the part number that appear after the frequency may not be marked on part (C of C provided)



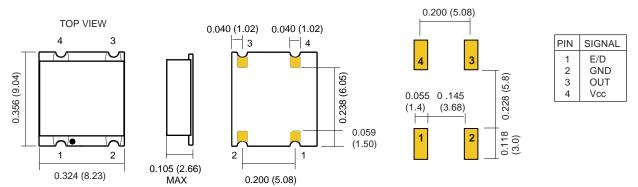
Consult factory for available frequencies and specs. Not all options available for all frequencies. A special part number may be assigned. Frequency Stability is inclusive of frequency shifts due to calibration, temperature, supply voltage, shock, vibration and load

# Mechanical: inches (mm)

### not to scale

# Solder Pads

Due to part size and factory abilities, part marking may vary from lot to lot and may contain our part number or an internal code.



Jun 2004