PLETRONICS PRONTO™ QM44L Series
CMOS Programmable Clock Oscillator

QM44L
3.2 x 2.5 x 0.95 mm
LCC Ceramic Package

Features

- Pletronics' QM44L Series is a programmable quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function (low standby power option)
- Low Jitter
- 1.8V, 2.5V, or 3.3V nominal Supply Voltage
- 1-200 MHz Frequency Range (1-125MHz at 1.8V)
- Fundamental crystals

Applications

Driving A/Ds, D/As, FPGAs
Digital Video
Ethernet, GbE
Medical
Storage Area Networking
COTS
Broad Band Access
SONET/ SDH/ DWDM
Test & Measurement

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Frequency Range [2]	1	-	200	MHz	(1.8V frequency range 1-125MHz)
Frequency Stability [2] ± 20 = **20**, ± 25 = **44**, ± 50 = **45**	±20	-	±50	ppm	For all supply voltages, load changes, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures
Operating Temperature Range [2]	-10 -20 -40	- - -	+70 +70 +85	°C	Standard range Extended range **C** option Extended range **E** option
Supply Voltage [1, 2] V_{CC}	1.8	-	3.3	V	± 5%, See Part Number options on page 3
Supply Current I_{CC}	-	-	-	mA	See Page 2
Output Waveform	CMOS				Cload = 15 pF
Duty Cycle	45	-	55	%	
Output V_{HIGH}	90	-	-	%V_{CC}	See Load Circuit and waveform page
Output V_{LOW}	-	-	10	%V_{CC}	
Output T_{RISE} and T_{FALL}	-	-	2	ns	
Startup Time	-	-	8	ms	Time for output to reach specified frequency
$V_{DISABLE}$	-	-	30	%	Of V_{CC} applied to Pad 1
V_{ENABLE}	70	-			
Enable Time	-	-	100	ns	Time for output to reach a logic state
Disable Time	-	-	100	ns	Time for output to reach a high Z state
Disable Current	- -	- 0.4	- -	mA	Enable/Disable: Pad 1 low, output disabled; See page 2 Standby option: Pad 1 low, output disabled, oscillator shutdown
Jitter	-	1.0	-	ps	12 kHz to 20 MHz @ 110 MHz
Storage Temperature Range	-55	-	+125	°C	

Notes: Specifications with Pad 1 E/D open circuit
   [1] Place an appropriate power supply bypass capacitor next to device for correct operation
   [2] Specified by part number

Parameter	Min	Typ	Max	Unit	Condition Vcc = 3.3V	
Supply Current I_{CC}			27 30 35 40	mA	1MHz ≤ Fo < 75MHz 75MHz ≤ Fo < 125MHz 125MHz ≤ Fo < 170MHz 170MHz ≤ Fo ≤ 200MHz	15pF load

Parameter	Min	Typ	Max	Unit	Condition Vcc = 2.5V	
Supply Current I_{CC}			27 30 35	mA	1MHz ≤ Fo < 75MHz 75MHz ≤ Fo < 125MHz 125MHz ≤ Fo ≤ 200MHz	15pF load

Parameter	Min	Typ	Max	Unit	Condition Vcc = 1.8V	
Supply Current I_{CC}			25	mA	1MHz ≤ Fo ≤ 125MHz	15pF load

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Part Number**

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V$_{CC}$	Frequency in MHz
QM44	45	L	E	V	- 125.0M
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20 = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	X = 1.8V ± 5% W = 2.5V ± 5% V = 3.3V ± 5%	1 - 200 MHz (1.8V: 1-125MHz)

** A custom part number is assigned for parts using the standby option

Device Marking

PRONTO
• *YMDxxx*

PRONTO = Pletronics Model
YMD = Date Code, Year Month Day (see below)
xxx = internal factory codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking.
External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3	4	5	6	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2022	2023	2024	2025	2026	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

RoHs Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

P/N:
QM4445LEV-125.0M
Customer P/N:
12345678
Qty:
1000
D/C
9DW
MSL: 1

RoHS Compliant
2nd LvL Interconnect
Category=e4
Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.026 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
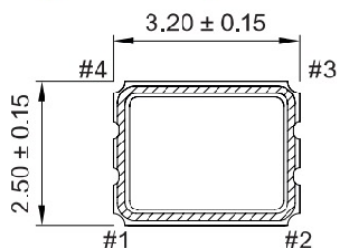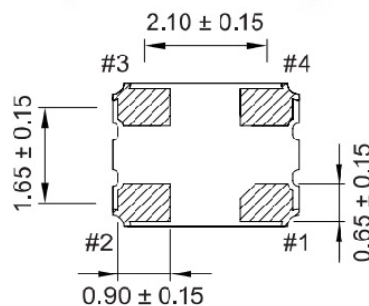Second Level Interconnect code: e4

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Mechanical Dimensions (mm)

[TOP VIEW]

3.20 ± 0.15

#4 #3

2.50 ± 0.15

#1 #2

[SIDE VIEW]

0.35

0.95 ± 0.15

[BOTTOM VIEW]

2.10 ± 0.15

#3 #4

1.65 ± 0.15

0.65 ± 0.15

#2 #1

0.90 ± 0.15
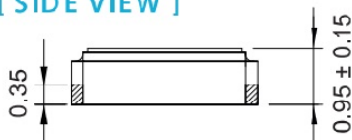
Pin#	Function
1	Tri-state
2	GND
3	Output
4	VDD

Enable/Disable

Pin 1	Output
Open	Active
Logic '1'	Active
Ground	Tri-state
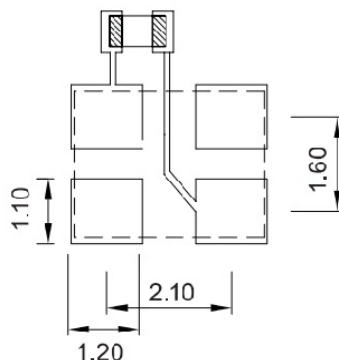
Pad Layout mm shown

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

1.60

1.10

2.10

1.20

To ensure optimal oscillator performance, place a by-pass capacitor of 0.01~0.1µF as close to the part as possible between Vdd and GND pads.

(Not to Scale)

Contacts (pads): Gold 11.8 to 39.4 µinches (0.3 to 1.0 µm) over Nickel 50 to 350 µinches (1.27 to 8.89 µm)

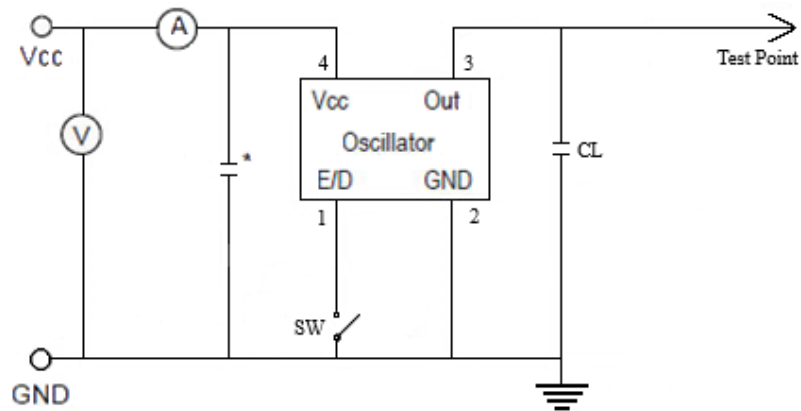For Optimum Jitter Performance, Pletronics recommends:
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
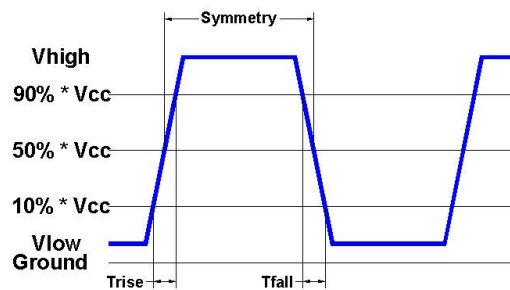- Do not place near piezoelectric buzzers or mechanical fans

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Electrical Test / Load Circuit

Notes:
CL: 15pF Includes the input capacitance of oscilloscope
* 0.01~0.1μF external by-pass filter is recommended

Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	MIL-STD-883 3015.7
Machine Model	200V	EIAJ ED-4701/304

Thermal Characteristics:
The maximum die or junction temperature is 125°C

Absolute Maximum Ratings

Parameter	Unit
V_{CC} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V_{CC} + 0.5V
Vo Output Voltage	-0.5V to V_{CC} + 0.5V

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Reflow Cycle

Maximum Reflow Conditions in accordance with IPC/JEDEC J-STD-020C "Pb-free"

The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	$(Ts_{max}$ to $T_P)$	3°C / second max	°C / s
Ramp down Rate	T_{cool}	6°C / second max	°C / s
Time 25°C to Peak Temperature	$T_{to-peak}$	8 minutes max	min
Preheat			
Temperature min	Ts_{min}	150	°C
Temperature max	Ts_{max}	200	°C
Time Ts_{min} to Ts_{max}	ts	60 – 180	sec
Soldering above liquidus			
Temperature liquidus	T_L	217	°C
Time above liquidus	t_L	60 – 150	sec
Peak temperature			
Peak Temperature	Tp	260	°C
Time within 5°C of peak temperature	tp	20 – 40	sec

Tape and Reel

Tape and Reel available for quantities of 250 to 3000 per reel, cut tape for < 250. 8mm tape, 4mm pitch.

10 PITCHES CUMULATIVE TOLERANCE ON TAPE +/- 0.2 mm

USER DIRECTION OF UNREELING

Embossment for Cavity Size See Note 1
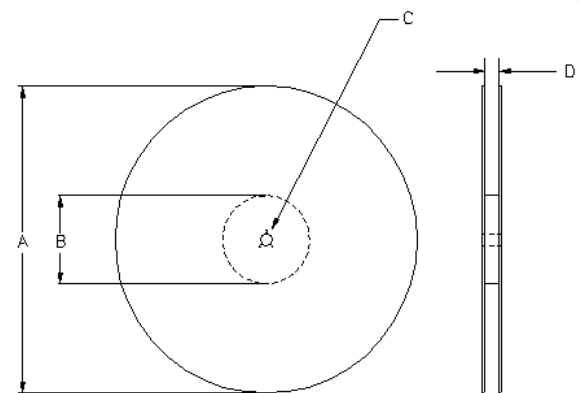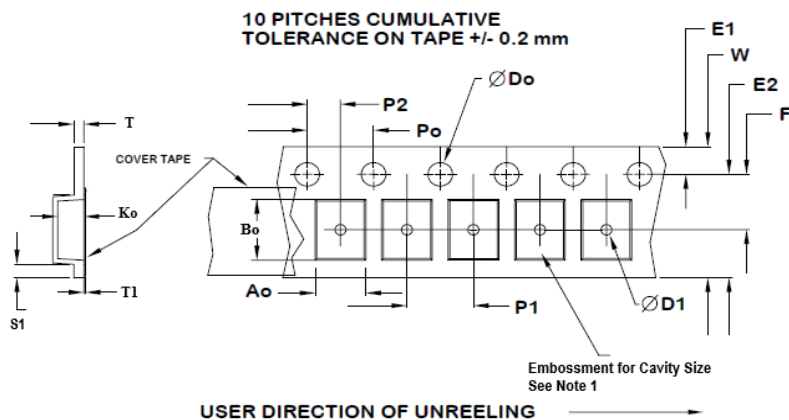
Tape Variable Dimensions   Table 2

Tape Size	E2 typ	F	P1	W max	Ao	Bo	Ko
8mm	6.25	3.5 ±0.05	4.0 ±0.1	8.2	2.7±0.1	3.4±0.1	1.4±0.1

Dimensions in mm   Drawing Not to scale
Note 1: Embossed cavity to conform to EIA– 481-B

Tape Constant Dimensions   Table 1

Tape Size	Do	D1 min	E1	Po	P2	S1 min	T max	T1 max
8mm	1.5 +0.1 -0.0	1.0	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.6	0.3	0.1

Reel Dimensions (may vary)   Table 3

Reel Size	A Inches	A mm	B Inches	B mm	C mm	D mm
7	7.0	177.8	2.50	63.5	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0
10	10.0	254.0	4.00	101.6		
13	13.0	330.2	3.75	95.3		

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