

PLETRONICS 3M77H 3eries 3.3V CMO5 Clock Oscillator







SM77HV 7.0 x 5.0 x 1.7 mm LCC Ceramic Package

Features

- Pletronics' SM77H Series is a quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function includes low standby power
- Low Jitte
- 3.3V nominal Supply Voltage
- Frequency Range 0.8 ~ 69.999MHz

Applications

Driving A/Ds, D/As, FPGAs Digital Video Ethernet, GbE Medical Storage Area Networking COTS Broad Band Access SONET/ SDH/ DWDM Base Stations/ Picocell Test & Measurement

Electrical Characteristics									
Parameter	Min	Тур	Max	Unit	Condition				
Frequency Range ²	0.80	-	69.999	MHz	Consult factory for other options				
Frequency Stability 2 $\pm 20 = 20^*$, $\pm 25 = 44$, $\pm 50 = 45$	±20	-	±50	ppm		load change, aging for 1 year at 25°C ± 2°C, es. *limited frequencies, see page 3			
Operating Temperature Range ²	-10 -20 -40	- - -	+70 +70 +85	°C	Standard range Extended range C option Extended range E option				
Supply Voltage 1,2 V _{CC}	2.97	3.3	3.63	V	3.3V ± 10%				
Output Waveform		(CMOS						
Duty Cycle	45	-	55	%	At 50% of V _{CC}				
Output V _{HIGH}	90	-	-	%	of V _{CC}	See Load Circuit			
Output V _{LOW}	-	-	10	%	of V _{CC}				
Startup Time	-	-	3	ms	Time for output to reach specified frequency				
V _{DISABLE}	-	-	30	- %	Of V applied to Red 1				
V _{ENABLE}	70	-	-	70	Of V _{CC} applied to Pad 1				
Enable Time	-	-	2	ms	Time for output to reach a logic s	state			
Disable Time	-	-	100	ns	Time for output to reach a high Z	state			
Enable/Disable Internal Pull-up	30	70	150	ΚΩ	To V _{CC}				
Output Leakage $V_{OUT} = V_{CC}$ $V_{OUT} = 0V$	-10 -10	-	+10 +10	μA					
Standby Current	-	-	10	μΑ	Pad 1 low, device disabled				
Jitter	-	-	0.6	ps RMS	12kHz to 20MHz from specified frequency				
Jillei	-	-	2.5	ps RMS	10Hz to 1MHz from specified frequency				
Storage Temperature Range	-55	-	+125	°C					

Notes: Specifications with Pad 1 E/D open circuit

Place an appropriate power supply bypass capacitor next to device for correct operation

² Specified by part number



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Electrical Characteristics

Parameter	Тур	Max	Unit	Condition	
	-	5.0		<35 MHz	CLOAD = 15 pF
Output T_{RISE} and T_{FALL}	-	3.0	nS	≥35 MHz	10% to 90% of V _{CC} See Load Circuit

Parameter	Тур	Max	Unit	Condition					
	-	9		<8 MHz					
V. Compt. Compat.(I.)	-	11		≥8 MHz and <16 MHz	0 45.5				
V _{CC} Supply Current (I _{CC})	-	17	- mA	≥16 MHz and <35 MHz	CLOAD = 15 pF				
	-	26		≥35 MHz					

Specifications with Pad 1 E/D circuit open



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Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V _{CC}	Frequency in MHz	Optional T&R Packaging code
SM77	45	Н	E	V	- 50.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	V = 3.3V ± 10%	0.80 - 69.999	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)

^{*} Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

Device Marking

PLE SM77 FFF.FF M • YMDxx PLE SM77 FFF.FF M • YYWWxx 7xYWWxx FFF.FF M • PLExxx

PLE = Pletronics
FFF.FF = Frequency in MHz
YMD or YWW or YYWW = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3		4	5	6	Cod	e	A	В	С	D	Е	F	:	G	Н	J	K	L	М
Year	2022	202	:3	2024	2025	2026	Mont	t h J/	AN	FEB	MAR	APR	MA'	Y JU	IN	JUL	AUG	SEP	OCT	NOV	DEC
Code	1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G	i				
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	5 16	3				
Code	Н	J	K	L	М	N	Р	R	Т	U	V	W	Χ	Υ	Z						
Day	17	18	19	20) 21	22	23	24	25	26	27	28	29	30	31						

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

Customer P/N:

1000

 RoHs Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.14 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

Second Level Interconnect code: e4



PLETRONICS 3M77H Series 3.3V CMOS Clock Oseillator

Mechanical Dimensions

	Inches	mm					
Α	0.276 ± 0.006	7.00 ± 0.15					
В	0.197 ± 0.006	5.00 ± 0.15					
С	0.067 Max	1.70 Max					
D ¹	0.038	0.96					
E ¹	0.200	5.08					
F ¹	0.004	0.10					
G ¹	0.043	1.10					
H ¹	0.055	1.40					
J ¹	0.024	0.60					
K ¹	0.008R	0.20R					
L ¹	0.102	2.60					
M¹	0.010	0.26					
N	End Detents optional						

Pad Layout mm shown
Disclaimer: Recommended layout shown.
Adjust layout as needed for individual process requirements.

Contacts (pads): Gold 11.8 to 39.4 µinches (0.3 to 1.0 µm) over Nickel 50 to 350 µinches (1.27 to 8.89 µm)

Layou	ayout								
Pad	Function	Note							
1	Output Enable/Disable	The oscillator shall operate when this pad is not connected. The output will be inhibited (high impedance state) when this pad is logic low. Recommend connecting this pad to $V_{\mathbb{CC}}$ if the oscillator is to be always on.							
2	Ground (GND)								
3	Output	CMOS							
4	V _{CC} Supply Voltage	Connect an appropriate power supply bypass capacitor as close as possible							

For Optimum Jitter Performance, Pletronics recommends:

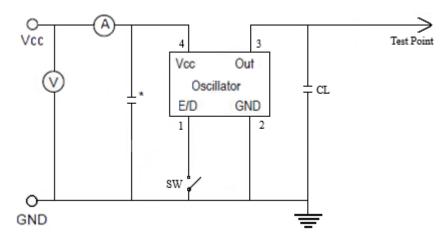
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

¹ Typical dimensions



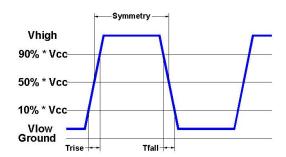
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Electrical Test / Load Circuit



Notes:

CL: Includes the input capacitance of oscilloscope * 0.01µF external by-pass filter is recommended



Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

Thermal Characteristics:

The maximum die or junction temperature is 150°C

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Machine Model	200V	JESD22-A115

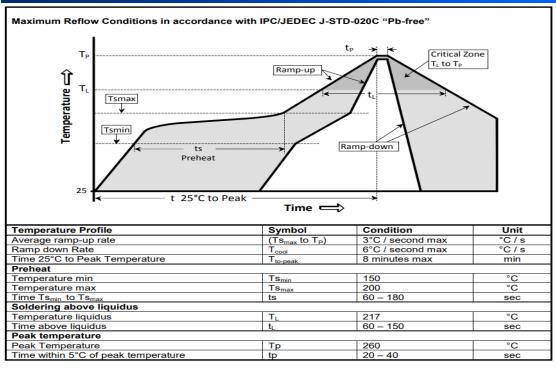
Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.3V to +4.0V
Vi Input Voltage	-0.3V to V _{CC} + 0.3V
Vo Output Voltage	-0.3V to V _{CC} + 0.3V



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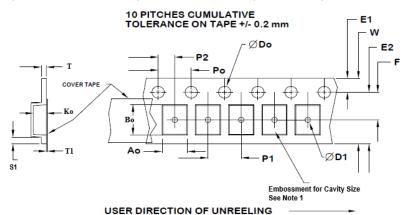
Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel

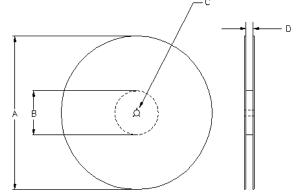
Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 16mm tape, 8mm pitch.



	Tape Variable Dimensions Table 2								
Tape Size	E2 typ	F	P1	W max	Ao	Во	Ko		
16mm	14.25	7.5 ±0.05	8.0 ± 0.1	16.3	5.56±0.1	7.85±0.1	2.0±0.1		

Dimensions in mm Drawing Not to scale Note 1: Embossed cavity to conform to EIA- 481-B

	Tape Constant Dimensions Table 1										
Tape Size	Do	D1 typ	E1	Po	P2	S1 min	T max	T1 max			
16mm	1.5	1.5	1.75	4.0	2.0	0.6	0.3	0.1			
TOTHIN	+0.1 -0.0	1.5	±0.1	±0.1	±0.1	0.0	0.5	0.1			



Reel Dimensions (may vary) Table 3						
	Α		В		С	D
Reel Size	Inches	mm	Inches	mm	mm	mm
7	7.0	177.8	2.50	63.5	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0
10	10.0	254.0	4.00	101.6		
13	13.0	330.2	3.75	95.3		



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