

PLETRONICS UHD4 Series TGXO / VGTGXO





UHD4 5.0 x 3.2 x 1.85 mm LCC Ceramic Package

Features

- Pletronics' UHD4 Series Temperature Compensated Crystal Oscillator
- Optional Voltage Control Function
- Low Power / Fast Warm Up
- CMOS Output
- 2.5V to 3.3V nominal Supply Voltage
- Not all combinations possible contact factory

Applications

SONET / SDH / DWDM
Test & Measurement
Telecom Transmission & Switching Equipment
Base Stations / Picocell
Wireless Communication Equipment

Electrical Characteristics							
Parameter	Min	Тур	Max	Unit	Condition (Consult factory for other options)		
Frequency Range ²	10	-	40	MHz	See table below for developed frequencies		
Frequency Stability vs. Temperature ²	-	-	±0.5 ±1.0	ppm	Over -30°C to +85°C Over -40°C to +85°C	at fixed V _{CC} + load (reference to midpoint min/max frequency) See factory for other options	
Frequency Initial Calibration		-	±2.0	ppm	Referenced to the value	≘ 25°C ± 2°C ; Vc=1.5V if VCTCXO	
Operating Temperature Range ²	-40	-	+85	°C	Contact factory for wide	er ranges	
Supply Voltage ^{1, 2} V _{CC}	2.5	-	3.3	Volts	± 5%		
Supply Current I _{CC}	-	2.5 3.0 4.0	-	mA	13 MHz 26 MHz Load: 15 pF, V _{CC} ± 5% 40 MHz		
Frequency Stability vs. Supply	-	-	±0.2	ppm	V _{CC} ± 5%		
Frequency Stability vs. Load	-	-	±0.2	ppm	Load: 15 pF ±10%		
Vcontrol Range	0.5	-	2.5	Volts	1.50 volts nominal for V _{CC}		
Frequency Pullability ²	0	±8.0	±12.0	ppm	Positive Slope		
Output Waveform		(CMOS				
Duty Cycle	40	50	60	%	Load: 15 pF; at 50% Vo	DD level	
Output VHIGH - VOH	90	-	-	%VDD			
Output VLow - VoL	-	-	10	%VDD			
Output TRISE/TFALL			6.5	ns	Load: 15pF; 10%~90%	VDD Levels	
Startup Time	-	-	10.0	mS	Within ± 2.0 ppm of final frequency		
Long Term Stability (Aging)	-	-	±1.0	ppm	First year at 25°C ± 2°C		
Phase Noise 100 Hz 1 kHz 10 kHz 100 kHz	-	-120 -134 -148 -150	-	dBc/Hz	z 25°C ± 2°C at 26.0 MHz		
Storage Temperature Range	-55	-	+95	°C			

Note: ¹ Place a 10r

The following is a list of developed frequencies. Consult factory for other options.

10.0M, 12.8M, 13.0M, 19.20M, 20.0M, 25.0M, 26.0M, 30.72M

¹ Place a 10nF power supply bypass capacitor next to device for correct operation

² Specified by part number.



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Part Number (Possible Options shown)									
Series	V _{cc} Suppl	Operating Temperature		Stability 1, 2	Pullability ¹	Frequency			
Model	Lowest Highest		Lowest	Highest	(ppm * 100)	(ppm)	(MHz)		
UHD4	031	031 035		K	050	008	-20.0M		
	031 = 3.1 for 3.3 volts nominal 029 = 2.9 for 3.0 volts nominal 027 = 2.7 for 2.8 volts nominal 024 = 2.4 for 2.5 volts nominal	035 = 3.5 for 3.3 volts nominal 031 = 3.1 for 3.0 volts nominal 029 = 2.9 for 2.8 volts nominal 026 = 2.6 for 2.5 volts nominal	J = -30°C L = -40°C	J = +80°C K = +85°C	050 = ± 0.5 100 = ± 1.0	000 = TCXO 005 = ±5 008 = ±8 012 = ±12	10 - 40MHz		

¹ Contact Factory for non-standard specifications

Device Marking ff.ffM = Frequency in MHz Specifications such as part number, frequency stability, supply voltage and = Date code (see table below) operating temperature range, etc. are not identified from marking. YMDPff.ffM = Pletronics External packaging labels and packing list will correctly identify the ordered = Internal Factory Codes Х **YMDxxx** Pletronics part number. 7 В C D Ε F G J Κ L М Code 3 4 5 6 Code Α Н 2023 2024 2025 2026 2027 Month JAN **FEB** MAR **APR** MAY JUN JUL **AUG** SEP OCT NOV DEC Year 2 С Χ Z Code 3 4 5 6 8 9 Α В D Ε G н Κ L M N R Т U 2 3 5 6 8 9 12 1 4 10 11 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 Day

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII RoHs Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

P/N: PLE Part Number

Customer P/N: PLE Part Number

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.10 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

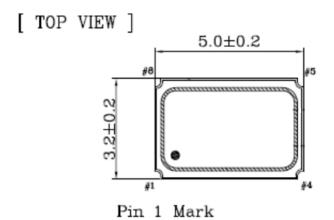
Second Level Interconnect code: e4

² Not all stabilities are available with all operating temperature ranges. Contact Factory for exact combinations available.

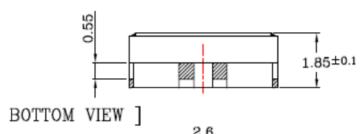


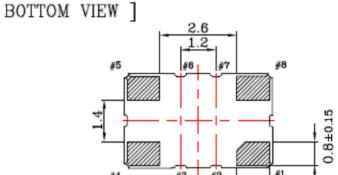
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Mechanical Dimensions (mm)



[SIDE VIEW]

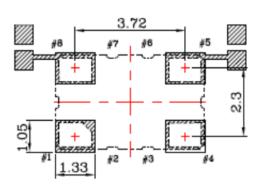




 1.1 ± 0.15

Recommended soldering pattern

★To ensure optimal oscillator performance, place a by—pass capacitor of 0.1uF as close to the part as possible between Vdd and GND pads.



Pad Layout

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

PIN#	FUNCTION
1	VCON:VC-TCXO
	NC:TCXO
4	GND
5	Fout
8	VDD

Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

All connection points in the designated region have solder mask cover to avoid any electrical connections (top view shown)

For Optimum Jitter Performance, Pletronics recommends:

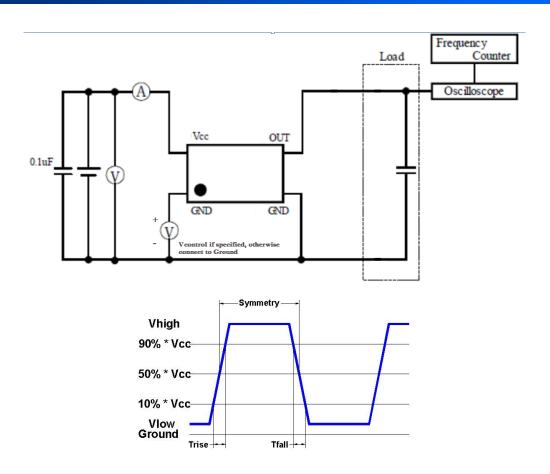
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

Production processing does not necessarily include testing of all parameters.

· Minimize air flow across the device

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Electrical Test /Load Circuit



Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.6V to +4.6V
Vi Input Voltage	-0.6V to V_{CC} + 0.6V and less than 4.6V
lo Output Current	-10mA to +10mA

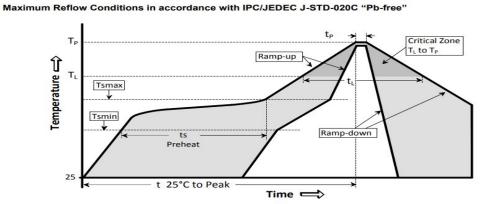
Thermal Characteristics:

The maximum die or junction temperature is 125°C



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Reflow Cycle

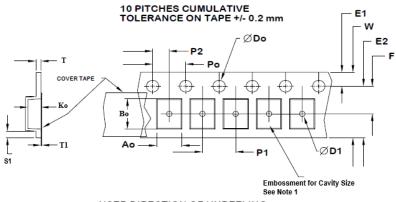


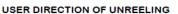
The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit	
Average ramp-up rate	(Ts _{max} to T _P)	3°C / second max	°C/s	
Ramp down Rate	T _{cool}	6°C / second max	°C/s	
Time 25°C to Peak Temperature	T _{to-peak}	8 minutes max	min	
Preheat	25 25 2	28 1111		
Temperature min	Ts _{min}	150	°C	
Temperature max	Ts _{max}	200	°C	
Time Ts _{min} to Ts _{max}	ts	60 - 180	sec	
Soldering above liquidus	89 ===	100 and 100 an	7.00 7.00	
Temperature liquidus	TL	217	°C	
Time above liquidus	t _L	60 - 150	sec	
Peak temperature				
Peak Temperature	Тр	260	°C	
Time within 5°C of peak temperature	tp	20 - 40	sec	

Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm or 16mm tape, 8mm pitch.





Tape Variable Dimensions Table 2							
Tape Size typ F P1 W Max Ao Bo Ko							
12mm	10.25	5.5 ±0.05	8.0 ±0.1	12.2	3.6±0.1	5.4±0.1	1.4±0.1
16mm	14.25	7.5 ±0.05	8.0 ± 0.1	16.3	3.6±0.1	5.4±0.1	1.4±0.1

Dimensions in mm Drawing Not to scale

Note 1: Embossed cavity to conform to EIA- 481-B

Tape Constant Dimensions Table 1								
Tape Size	Do	D1 min	E1	Ро	P2	S1 min	T max	T1 max
12mm	1.5	1.5	1.75	4.0	2.0 ±0.05	0.6	0.3	0.1
16mm	+0.1 -0.0	1.5	±0.1	±0.1	2.0	0.6	0.3	0.1

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Reel Dimensions (may vary) Table 3								
		A	O	D				
Reel Size	Inches	mm	Inches	mm	mm	mm		
7	7.0	177.8	2.50	63.5	13.0	Tape size +0.4		
10	10.0	254.0	4.00	101.6	+0.5 -0.2			
13	13.0	330.2	3.75	95.3		+2.0 -0.0		



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