







Note:



OeD4 5.0 x 3.2 x 1.85 mm LCC Ceramic Package

Features

- Temperature Compensated Crystal Oscillator
- Optional Voltage Control Function
- Low Power / Fast Warm Up
- CMOS or Clipped Sine Wave Output
- 2.8V to 3.3V nominal Supply Voltage
- See table for developed Frequencies

Applications

SONET / SDH / DWDM Test & Measurement Telecom Transmission & Switching Equipment Base Stations / Picocell Wireless Communication Equipment

Electrical Characteristics for CM	os				
Parameter	Min	Тур	Max	Unit	Condition (Consult factory for other options)
Frequency Range ²	8.192	1	40.0	MHz	See table below for developed frequencies
Frequency Stability vs. Temperature ²		-	±200 ±100 ±50	ppb	Over -40°C to +85°C Over -10°C to +70°C Over 0°C to +70°C Over 0°C to +70°C at fixed V _{CC} + load (reference to midpoint min/max frequency) See factory for other options
Frequency Initial Calibration		-	±2.0	ppm	Vcontrol 1.50 volts at 25°C ± 2°C when V _{CC} ≥ 2.8 volts, If Vcontrol used
Operating Temperature Range ²	-40	-	+85	°C	Widest range available
Supply Voltage ^{1, 2} V _{CC}	2.8	-	3.3	Volts	± 5%
Supply Current ² I _{CC}	-	3.5 4.5 6.5	-	mA	10 MHz 25 MHz Load: 15 pF, V _{CC} ± 5% 40 MHz
Frequency Stability vs. Supply	-	-	±0.2	ppm	Load: 15 pF, V _{CC} ± 5%
Frequency Stability vs. Load	-	-	±0.2	ppm	Load: 15 pF ± 10%
Vcontrol Range	0.5	-	2.5	Volts	1.50 volts nominal
Frequency Pullability ²	0	±8.0	±12.0	ppm	Positive Slope
Linearity	-	-	2.0	%	
Output Waveform		С	MOS		
Duty Cycle	40	50	60	%	Load: 15 pF
Output V _{HIGH}	90	ı	-	%Vcc	Vth: T_R and T_F 10% and 90% of Vcc
Output V _{LOW}	-	-	10	%Vcc	Vth: D.C. 50% of Vcc
Output T _{RISE} and T _{FALL}	-	-	6.5	ns	
Startup Time	-	-	10.0	ms	Within ± 2.0 ppm of final frequency
Long Term Stability (Aging)	-	-	±1.0	ppm	Per year at 25°C ± 2°C
Phase Noise 100 Hz 1 kHz 10 kHz 100 kHz	-	-120 -134 -144 -145	-	dBc/Hz	25°C ± 2°C at 20.0 MHz
Jitter	-	0.6	-	pS	Frequency offset from carrier 12 kHz to 20 MHz, Typical performance at 20.0 MHz
Storage Temperature Range	-55	-	+95	°C	

Rev. F Product information is current as of publication date. The product conforms to specifications per the terms of the Pletronics standard warranty. Feb 16, 2023 Production processing does not necessarily include testing of all parameters.

Place a 10nF power supply bypass capacitor next to device for correct operation
 Typical capabilities shown. A unique OeXO[®] datasheet is created for each specific device. See Factory for other options.



PLETRONICS OeD4 Series OeXO® Oscillator

Electrical Characteristics for Clip	ped Sir	ne Wave							
Parameter	Min	Тур	Max	Unit	Condition (Consult fa	actory for other options)			
Frequency Range ²	8.192	-	40.0	MHz	See table below for dev	eloped frequencies			
Frequency Stability vs. Temperature ²	-200 -100 -50	-	+200 +100 +50	ppb	$\begin{array}{cccccccccccccccccccccccccccccccccccc$				
Frequency Initial Calibration		-	±2.0	ppm	Vcontrol 1.50 volts at 25°C ± 2°C when V _{CC} ≥ 2.8 volts If Vcontrol				
Operating Temperature Range ²	-40	-	+85	°C	Widest range available				
Supply Voltage ^{1, 2} V _{CC}	2.8	-	3.3	V	± 5%				
Supply Current ² I _{CC}	-	2.0 3.0 4.0	-	mA	10 MHz 25 MHz 40 MHz	Load: 10 Kohm 10 pF, V _{CC} ± 5%			
Frequency Stability vs. Supply	-	-	±0.2	ppm	Load: 10 Kohm 10 pF	, V _{CC} ± 5%			
Frequency Stability vs. Load	-	-	±0.2	ppm	Load: 10 Kohm 10 pF	± 10%			
Vcontrol Range	0.5	-	2.5	Volts	1.50 volts nominal for V _{CC}				
Frequency Pullability ²	0	±8.0	±12.0	ppm	Positive Slope				
Linearity	-	-	2.0	%					
Output Waveform		Clipped	Sine Wav	е	Dc Coupled				
Output Level	0.8	ı	-	V p-p	Load: 10 Kohm 10 pF	± 10%			
Startup Time	-	ı	10.0	mS	Within ± 2.0 ppm of fina	I frequency			
Long Term Stability (Aging)	-	-	±1.0	ppm	Per year at 25°C ± 2°C				
Phase Noise 100 Hz 1 kHz 10 kHz 100 kHz	-	-120 -134 -144 -145	-	dBc/Hz	25°C ± 2°C at 20.0 MHz				
Jitter	-	0.6	-	ps	Frequency offset from c Typical performance at	arrier 12 kHz to 20 MHz, 20.0 MHz			
Storage Temperature Range	-55	-	+95	°C					

Note:

The following is a list of developed frequencies. Consult factory for other options.

10.00M, 16.384M, 20.00M, 25.00M, 40.00M

¹ Place a 10nF power supply bypass capacitor next to device for correct operation
² Typical capabilities shown. A unique OeXO[®] datasheet is created for each specific device. See Factory for other options.



PLETRONICS OaD4 Series OeXO® Oscillator

Part Nu	Part Number (Possible Options shown)												
Series	V. Supply Voltage 1	Operating	Temperature	Stability 1, 2	Pullability ¹	Frequency							
Model	V _{cc} Supply Voltage ¹	Lowest Highest		(ppm)	(ppm)	(MHz)							
OED4	A unique num	-19.44M											
	3.3 volts nominal 3.0 volts nominal 2.8 volts nominal	0°C -10°C -20°C -30°C -40°C	+70°C +80°C +85°C	± 0.05 ± 0.1 ± 0.2	0 ±5 ±8	8.192 - 40 MHz							

¹ Contact Factory for non-standard specifications

Device Marking

YMD FFFF.zzz YMD OR FFFF. zzz PLEzzzzz YMD

FFFF = Crystal Frequency in MHz (See Note below)

z = Internal factory codes

PLE = Pletronics

YMD = Date code (may appear in one of 3 locations shown)

Note: Output Frequency may be half the Crystal Frequency marking, depending on requirements.

Specifications such as part number, frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code		3		4		5	5	6	3	7	'	Co	de	Α		В	С		D	Е		F	G		Н	J		K	L		М
Year	2	023	3	202	24	202	25	20	26	202	27	Mor	nth	JAN	1 I	EB	MA	R	APR	MA	Υ,	JUN	JUL	Al	UG	SEP	C	СТ	NOV	′ D	EC
Code	1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G	Н	J	K	L	М	N	Р	R	Т	U	٧	W	Х	Υ	Z

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII



RoHs Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant
2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.10 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

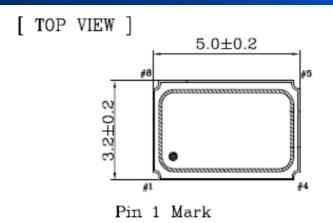
Second Level Interconnect code: e4

² Not all stabilities are available with all operating temperature ranges. Contact Factory for exact combinations available.

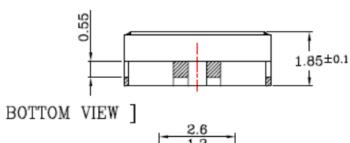


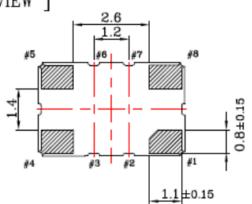
PLETRONICS OaD4 Series OaXO® Oscillator

Mechanical Dimensions



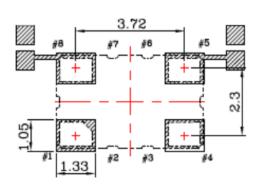
[SIDE VIEW]





Recommended soldering pattern

★To ensure optimal oscillator performance, place a by—pass capacitor of 0.1uF as close to the part as possible between Vdd and GND pads.



Pad Layout (Top View)

Disclaimer: Recommended layout shown. Adjust pad layout as needed for individual process requirements. Solder mask required, as shown.

PIN#	FUNCTION
1	VCON:VC-TCXO
	NC:TCXO
4	GND
5	Fout
8	VDD

Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

For Optimum Jitter Performance, Pletronics recommends:

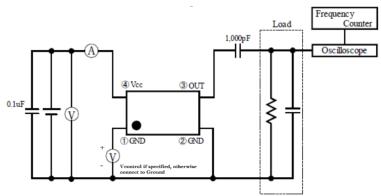
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans
- Minimize air flow across the device



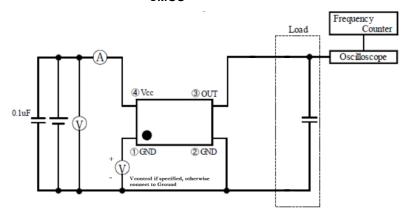
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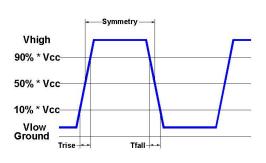
Electrical Test /Load Circuit

Clipped Sine Wave



CMOS





Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

Thermal Characteristics:

The maximum die or junction temperature is 125°C

ESD Rating

Model	Min. Voltage	Condition				
Human Body Model	2000V	JESD22-A114				
Machine Model	200V	JESD22-A115				

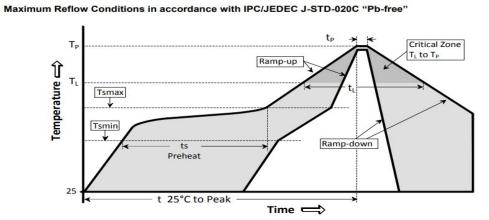
Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.6V to +4.6V
Vi Input Voltage	-0.6V to V _{CC} + 0.6V
lo Output Current	-10mA to +10mA



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Reflow Cycle

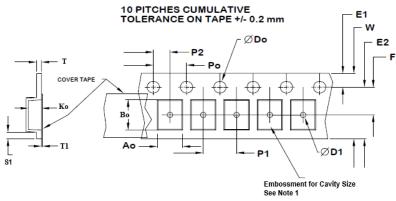


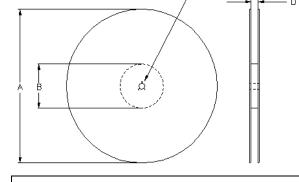
The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	(Ts _{max} to T _P)	3°C / second max	°C/s
Ramp down Rate	T _{cool}	6°C / second max	°C/s
Time 25°C to Peak Temperature	T _{to-peak}	8 minutes max	min
Preheat	7/2 A	20 22 22 22 22 22 22 22 22 22 22 22 22 2	72 73
Temperature min	Ts _{min}	150	°C
Temperature max	Ts _{max}	200	°C
Time Ts _{min} to Ts _{max}	ts	60 – 180	sec
Soldering above liquidus	% ===	(C)	*
Temperature liquidus	TL	217	°C
Time above liquidus	tL	60 - 150	sec
Peak temperature			
Peak Temperature	Тр	260	°C
Time within 5°C of peak temperature	tp	20 – 40	sec

Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm tape, 8mm pitch.





USER DIRECTION OF UNREELING

	Tape Variable Dimensions Table 2											
Tape Size	Tape E2 F P1 W Ao Bo Ko											
12mm	10.25	5.5 ±0.05	8.0 ±0.1	12.2	3.5±0.1	5.3±0.1	1.9±0.1					

Dimensions in mm Drawing Not to scale Note 1: Embossed cavity to conform to EIA- 481-B

	Tape Constant Dimensions Table 1										
Tape Size	Do	D1 min	E1	Ро	P2	S1 min	T max	T1 max			
12mm	1.5	1.5	1.75	4.0	2.0	0.6	0.3	0.1			
12111111	+0.1 -0.0	1.5	±0.1	±0.1	±0.05	0.0	0.5	0.1			

	Reel Dimensions (may vary) Table 3												
		A	В		С	D							
Reel Size	Inches	mm	Inches	mm	mm	mm							
					13.0	Tape size +0.4							
7	7.0	180	2.50	60	+0.5 -0.2	+2.0 -0.0							



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Contacting Pletronics Inc.

Pletronics, Inc. 19013 36th Ave. West Lynnwood, WA 98036-5761 U.S.A. Tel: 425.776.1880 Fax: 425.776.2760

email: ple-sales@pletronics.com

URL: www.pletronics.com