

### PLETRONICS SM44T Series 1.8V CMOS Clock Oscillator







SM44TX 3.2 x 2.5 x 1.05 mm LCC Ceramic Package

### **Features**

- Pletronics' SM44T Series is a quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function includes low standby power
- Low Jitter
- 1.8V nominal Supply Voltage
- 0.80-100 MHz Frequency Range

### **Applications**

Driving A/Ds, D/As, FPGAs Digital Video Ethernet, GbE Medical Storage Area Networking COTS **Broad Band Access** SONET/ SDH/ DWDM Base Stations/ Picocell Test & Measurement

Electrical Characteristics									
Parameter	Min	Тур	Max	Unit	Condition				
Frequency Range <sup>2</sup>	0.80	-	100	MHz	Consult factory for other options				
Frequency Stability $^2$ $\pm 20 = 20^*$ , $\pm 25 = 44$ , $\pm 50 = 45$	±20	-	±50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C 2°C, shock, vibration and temperatures. *limited frequencies, see page				
Operating Temperature Range <sup>2</sup>	-10 -20 -40		+70 +70 +85	°C	Standard range Extended range C option Extended range E option				
Supply Voltage <sup>1, 2</sup> V <sub>CC</sub>	1.62	1.80	1.98	V	1.8V ± 10%				
Output Waveform		С	MOS						
Duty Cycle	45	-	55	%	at 50% of V <sub>CC</sub>				
Output V <sub>HIGH</sub>	0.9Vcc	-	-	V		See Load Circuit			
Output V <sub>LOW</sub>	-	-	0.1Vcc	V					
Startup Time	-	-	10	ms	Time for output to reach specified frequency	uency			
V <sub>DISABLE</sub>	-	-	0.3Vcc	V					
V <sub>ENABLE</sub>	0.7Vcc	-	-	V					
Output Enable Time	-	-	250	ns	Time for output to reach a logic state				
Output Disable Time	-	-	250	ns	Time for output to reach a high Z state				
Enable/Disable Internal Pull-up	30	70	150	ΚΩ	To V <sub>CC</sub>				
Output Leakage $V_{OUT} = V_{CC}$ $V_{OUT} = 0V$	- -10	-	+10	μA					
Standby Current	-	-	10	μΑ	Pad 1 low, device disabled				
Phase Jitter, rms 1 to 15MHz ≥15 to 35MHz ≥35 to 50MHz > 50MHz	-	-	6.0 5.0 4.0 3.0	ps RMS	10 Hz to 1 MHz from the output freque	ncy			
Phase Jitter, rms Output ≥ 40MHz	-	-	0.7	ps RMS	12 kHz to 20 MHz from the output freq	uency			
Storage Temperature Range	-55	-	+125	°C					

Notes: Specifications with Pad 1 E/D open circuit

Place an appropriate power supply bypass capacitor next to device for correct operation

<sup>2</sup> Specified by part number



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<b>Electrical Characteristics</b>					
Parameter	Тур	Max	Unit	Condition	_
	2	5		< 35 MHz	0 45.5
Output $T_{\text{RISE}}$ and $T_{\text{FALL}}$	1	3.5	nS	≥ 35 MHz and < 70 MHz	CLOAD = 15 pF 10% to 90% of V <sub>CC</sub> See Load Circuit
	1.5	2.5		≥ 70 MHz	
	1	2		< 8 MHz	
	1.5	2.5		≥ 8 MHz and < 16 MHz	
Supply Current (I <sub>CC</sub> )	2	3	mA	≥ 16 MHz and < 35 MHz	C <sub>LOAD</sub> = 15 pF
	12	18		≥ 35 MHz and <70 MHz	
	17	27		≥ 70 MHz and <100 MHz	

Specifications with Pad 1 E/D circuit open



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Part Number										
Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V <sub>cc</sub>	Frequency in MHz	Optional T&R Packaging code				
SM44	45	Т	E	X	- 100.0M	-XX				
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20 = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	<b>X</b> = 1.8V ±10%	0.80 - 100 MHz	T250 = 250 per Reel T500 = 500 per Reel T3K = 3000 per Reel (Std)				

### **Device Marking**

PFF.FF MYMDxx

• YMxxx

P = Pletronics FF.FF = Frequency in MHz

YMD or YM = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3		4	5		6	Cod	е	A	В	С	D	Е	ı	•	G	Н	J	K	L	M
Year	2022	202	:3	2024	202	5	2026	Mont	h J	AN	FEB	MAR	APR	MA	Y JL	JN	JUL	AUG	SEP	OCT	NOV	DEC
																			•	•	•	
Code	1	2	3	4		5	6	7	8	9	Α	В	С	D	Е	F	G	;				
Day	1	2	3	4		5	6	7	8	9	10	11	12	13	14	15	5 16	ŝ				
Code	Н	J	K	L		М	N	Р	R	Т	U	V	w	X	Υ	Z						
Day	17	18	19	20	) :	21	22	23	24	25	26	27	28	29	30	31						

### **Package Labeling**

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

**RoHS Compliant** 

2nd LvL Interconnect Category=e4

Max Safe Temp=260C for 10s 2X Max

### Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.024 grams

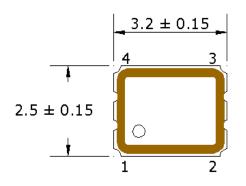
Moisture Sensitivity Level: 1 As defined in J-STD-020D

Second Level Interconnect code: e4



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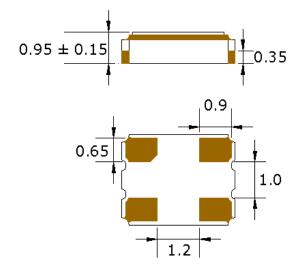
### **Mechanical Dimensions**

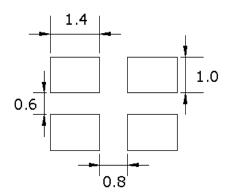


### Pad Connections

Pad	Function
1	Enable/Disable
2	Ground
3	Output
4	Vcc

ENABLE/DISABLE					
Pad 1	Output				
VIH/Open	Active				
VIL/Gnd	Disabled/Tristate				





### Dimensions in mm

**Pad Layout** Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

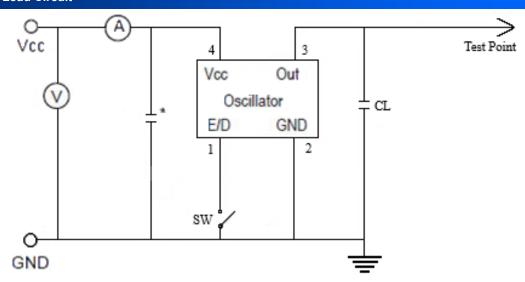
For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



### PLETRONICS 3M44T Series 1.8V CMOS Clock Oscillator

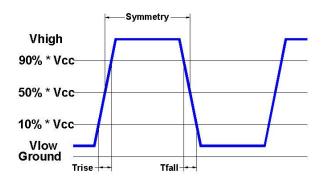
### **Electrical Test / Load Circuit**



Notes:

CL: Includes the input capacitance of oscilloscope

<sup>\* 0.01</sup>µF external by-pass filter is recommended



### **Environmental / ESD Ratings**

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

#### **Thermal Characteristics:**

The maximum die or junction temperature is 150°C

### **ESD Rating**

Model	Min. Voltage	Condition
Human Body Model	2000V	MIL-STD-883 3015.7
Machine Model	200V	EIAJ ED-4701/304

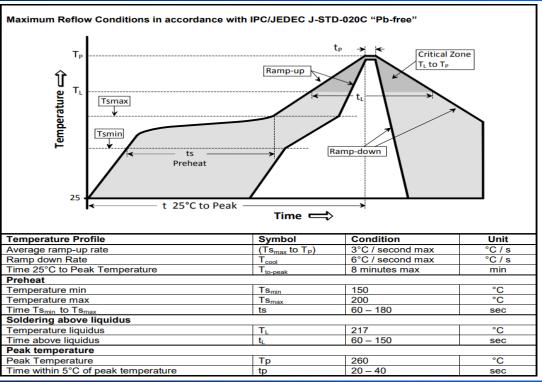
### Absolute Maximum Ratings

Parameter	Unit
V <sub>CC</sub> Supply Voltage	-0.3V to +4.0V
Vi Input Voltage	-0.3V to V <sub>CC</sub> + 0.3V
Vo Output Voltage	-0.3V to V <sub>CC</sub> + 0.3V



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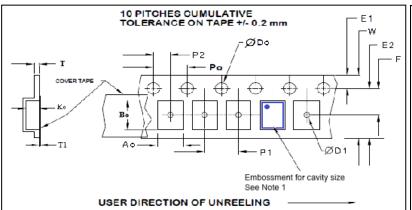
### **Reflow Cycle**



The part may be reflowed 2 times without degradation (typical for lead free processing).

### Tape and Reel

Tape and Reel available for quantities of 250 to 3000 per reel, cut tape for < 250. 8mm tape, 4mm pitch.



A B	

Tape Variable Dimensions Table 2										
Tape Size	E2 typ	F	P1	W max	Ao	Во	Ko			
8mm	6.25	3.5 ±0.05	4.0 ±0.1	8.2	2.7±0.1	3.4±0.1	1.4±0.1			

Reel Dimensions (may vary) В С D Reel Inchmm Inches mm mm mm Size es Tape size 13.0 +0.4 7 7.0 180 2.50 63.5 +0.5 -0.2 +2.0

Table 3

Dimensions in mm Drawing Not to scale Note 1: Embossed cavity to conform to EIA- 481-B

Tape Constant Dimensions Table 1											
Tape Size	Do	D1 typ	E1	Po	P2	T max	T1 max				
8mm	1.5 +0.1 -0.0	1.0	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.3	0.1				



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