







LV55F/G 5.0 x 3.2 x 1.35mm LCC Ceramic Package

Features

- Pletronics' LV55F/G Series is a Quartz crystal controlled Precision Square Wave Oscillator
- LVDS Output
- Enable/Disable Function on pad 1
- Low Jitter
- 3.3V nominal Supply Voltage
- 13.5 220 MHz Frequency Range

Applications

PON

Driving A/Ds, D/As, FPGAs Fibre Channel Ethernet, GbE, SynchE Medical Storage Area Networking COTS Telecom

Parameter	
Frequency Range 35	
± 20 = 20°, ± 25 = 44, ± 50 = 45 ±20 - ±30 ppliii 2°C, shock, vibration and temperatures. *limited frequencies Operating Temperature Range 2 -10 - +70 +7	
Operating Temperature Range 2 -20	
Supply Current I _{CC} - 16 27 mA 280 MHz 'F' Series ≥80 MHz - 16 27 mA 270 MHz to <125 MHz - 20 34 4 24 40 MA 212 MHz - 20 34 4 24 40 MA 2125 MHz - 160 MHz - 2160 MHz - 300 MH	
Supply Current I _{CC}	
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$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	int to Point)
Differential Output Error ΔV_{OD} 50 mV Output High Level V_{OH} - 1.43 1.6 V Output Low Level V_{OL} 0.9 1.1 - V Output T_{RISE} and T_{FALL} 0.15 0.4 ns Vth is 20% and 80% output swing Startup Time 2 ms Time for output to reach specified frequency Duty Cycle $\leq 200 \text{ MHz} + 45 \\ > 200 \text{ MHz} + 40 + 60 \\ V_{DISABLE} \text{ VIL}$ 0.3Vcc V Referenced to Ground $V_{ENABLE} \text{ VIH}$ 0.7Vcc 2 ms Time for output to reach a logic state	
Output T_{RISE} and T_{FALL} Output T_{RISE} and T_{RISE} Output T_{RISE} and T_{RISE} Output T_{RISE} and T_{RISE} Output T_{RIS	
Duty Cycle ≤ 200 MHz 45 > 200 MHz - 55 60 % At output crossing point V _{DISABLE} VIL 0.3Vcc V Referenced to Ground V _{ENABLE} VIH 0.7Vcc 2 ms Time for output to reach a logic state	
VDISABLE VIL - - 0.3Vcc V Referenced to Ground VENABLE VIH 0.7Vcc - - - 2 ms Time for output to reach a logic state	
V _{ENABLE} VIH 0.7Vcc V Referenced to Ground Enable Time 2 ms Time for output to reach a logic state	
V _{ENABLE} VIH 0.7Vcc V Referenced to Ground Enable Time 2 ms Time for output to reach a logic state	
Enable Time 2 ms Time for output to reach a logic state	
B. 11 1 1 1 1 1 1 1	
Disable Time - - 200 ns Time for output to reach a high Z state	
Output Leakage $V_{OUT} = V_{CC}$ - +10 μA Pad 1 low, device disabled	
Standby Current 10 µA	
Jitter - 0.6 ps 12 kHz to 20 MHz from the output frequency 10 Hz to 1 MHz from the output frequency	
Phase Noise 100 Hz 1 kHz -129 10 kHz 100 kHz 1 100 kHz 1 1 MHz 20 MHz 20 MHz 157 157 157 158 159 159 159 159 159 159 159 159 159 159	
Storage Temperature Range -55 - +125 °C	

Notes: Specifications with Pad 1 E/D open circuit

1 Place an appropriate power supply bypass capacitor next to device for correct operation

² Specified by part number



Part Number

Series Model			Operating Temperature Range	Supply Voltage V _{cc}	Frequency in MHz	Optional T&R Packaging code
LV55	45	F	E	V	- 100.0M	-xx
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	V = 3.3V ± 10%	13.5 - 220 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)

^{*} Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

Device Marking

• YMDxxx

P = Pletronics

FFF.FF L = Frequency in MHz, L for LVDS

t = Version, F or G

YMD = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3		4	5	6	Code	е	A	В	С	D	Е	F	:	G	Н	J	K	L	М
Year	2022	202	:3	2024	2025	2026	Mont	h J	AN I	FEB	MAR	APR	MA	Y JU	IN	JUL	AUG	SEP	OCT	NOV	DEC
Code	1	2	3	4	5	6	7	8	9	Α	В	С	D	E	F	G	i				
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	3				
Code	Н	J	K	L	М	N	Р	R	T	U	٧	w	Х	Υ	Z						
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31						

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

1000 MSL: 1 RoHS Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

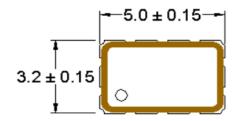
Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.055 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

Second Level Interconnect code: e4



Mechanical Dimensions

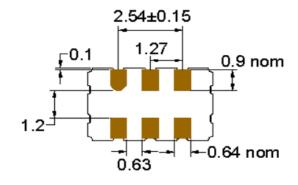




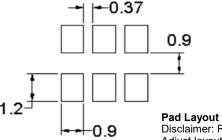
Pad Connections

Pad	Function
1	Enable/Disable
2	NC
3	Ground
4	Output
5	Output N
6	Vcc

ENABLE/DISABLE								
Pad 1	Outputs							
Vɪн/Open	Active							
VIL/Gnd	Disabled/Tristate							



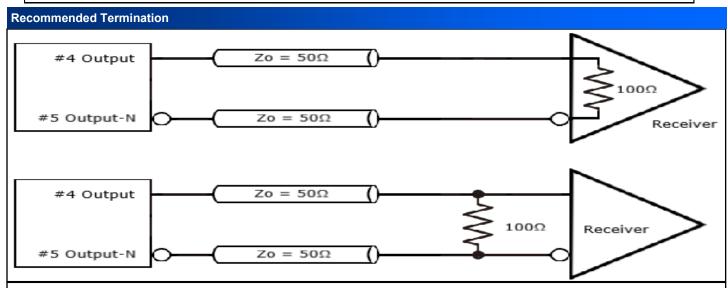
Solder Pad Layout



Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

Dimensions in mm

Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)



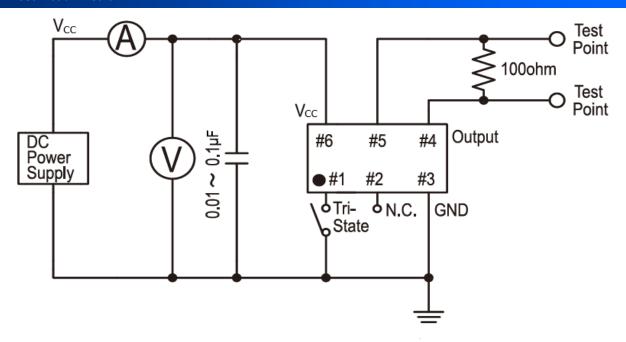
For any other terminations, the oscillator should be sampled and tested in the application. Both outputs shall be terminated and biased for proper operation.

For Optimum Jitter Performance, Pletronics recommends:

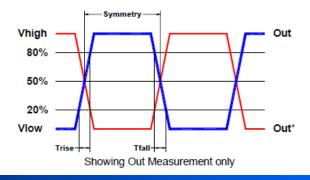
- · A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



Electrical Test /Load Circuit



Test Waveform



Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

Thermal Characteristics:

The maximum die or junction temperature is 150°C

ESD Rating

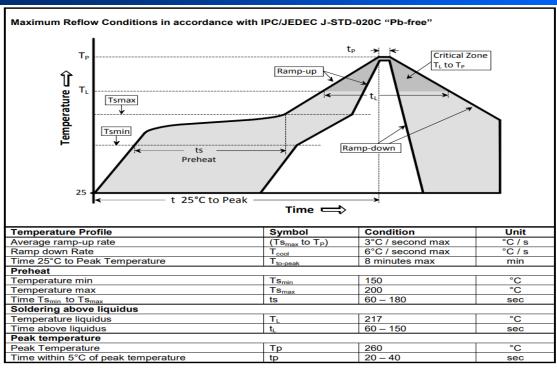
Model	Min. Voltage	Condition		
Human Body Model	2000V	JESD22-A114		
Machine Model	200V	JESD22-A115		

Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V



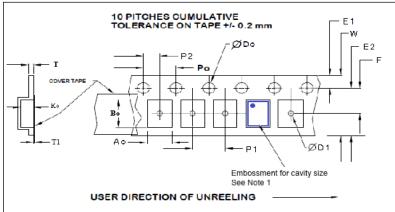
Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel

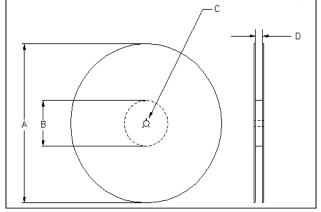
Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm (or 16mm) tape, 8mm pitch.



Tape Variable Dimensions Table 2									
Tape Size	E2 typ	F	P1	W max	Ao	Во	Ko		
12mm	10.25	5.5 ±0.05	8.0 ±0.1	12.2	3.6±0.1	5.4±0.1	1.4±0.1		
16mm	14.25	7.5 ±0.05	8.0 ± 0.1	16.3	3.6±0.1	5.4±0.1	1.4±0.1		

Drawing Not to scale Dimensions in mm Note 1: Embossed cavity to conform to EIA- 481-B

Tape Constant Dimensions Table 1										
Tape Size	Do	D1 typ	E1	Ро	P2	T max	T1 max			
12mm	1.5 +0.1	1.5	1.75	4.0	2.0 ±0.05	0.3	0.1			
16mm	+0.1 -0.0	1.5	±0.1	±0.1	2.0 ±0.1	0.3	0.1			



Reel Dimensions (may vary) Table 3											
		A	С	D							
Reel Size	Inches	mm	Inches	mm	mm	mm					
7	7.0	180	2.50	60	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0					



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Contacting Pletronics Inc.

Pletronics, Inc. 19013 36th Ave. West Lynnwood, WA 98036-5761 U.S.A. Tel: 425.776.1880 Fax: 425.776.2760

email: ple-sales@pletronics.com URL: www.pletronics.com