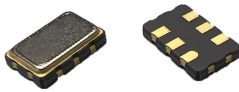




PLETRONICS PE55F/G Series 2.5V PECL Clock Oscillator



PE55F/G
5.0 x 3.2 x 1.3 mm
LCC Ceramic Package

Features

- Pletronics' PE55F/G Series is a Quartz crystal controlled Precision Square Wave Oscillator
- PECL Differential Output
- 'F' series is Fundamental; 'G' series is 3rd OT
- Enable/Disable Function on pad 1
- Low Jitter
- 2.5V nominal Supply Voltage

Applications

Driving A/Ds, D/As, FPGAs
Fibre Channel
Ethernet, GbE, SynchE
Medical
Storage Area Networking
COTS
Telecom
PON

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Frequency Range ²	13.5 35	- -	110 220	MHz	'F' Series 'G' Series
Frequency Stability ² $\pm 20 = 20^*$, $\pm 25 = 44$, $\pm 50 = 45$	± 20	-	± 50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C \pm 2°C, shock, vibration and temperatures. *limited frequencies, see page 2
Operating Temperature Range ²	-10 -20 -40	-	+70 +70 +85	°C	Standard range Extended range C option Extended range E option
Supply Voltage ^{1,2} V _{CC}	2.25	2.5	2.75	V	
Supply Current I _{CC} 'F' Series	-	33 34	44 48	mA	< 80 MHz ≥ 80 MHz
Supply Current I _{CC} 'G' Series	- - - -	33 34 35 37	44 48 50 54	mA	< 90 MHz ≥ 90 MHz ~ 125 MHz ≥ 125 MHz ~ 160 MHz ≥ 160 MHz
Output Waveform	PECL				
Output High Level V _{OH}	1.475	1.55	1.62	V	Referenced to Ground
Output Low Level V _{OL}	0.69	0.8	0.88	V	Referenced to Ground
Output T _{RISE} and T _{FALL}	-	0.2	0.4	ns	V _{th} is 20% and 80% levels of output swing
Start Up Time	-	-	2	ms	Time for output to reach specified frequency
Duty Cycle	45	-	55	%	At output crossing point
V _{DISABLE}	-	-	0.3V _{CC}	V	Referenced to ground
V _{ENABLE}	0.7V _{CC}	-			
Enable Time	-	-	2	ms	Time for output to reach a logic high state
Disable Time	-	-	200	ns	Time for output to reach a high Z state
Enable/Disable Internal Pull-up	-	39	-	k Ω	Pin 1 open or High
Output Leakage V _{OUT} = V _{CC} V _{OUT} = 0V	-10 -10	-	+10 +10	μ A	Pad 1 low, device disabled
Standby Current	-	-	10	μ A	
Jitter	-	-	0.6	ps rms	12 kHz to 20 MHz from the output frequency
	-	-	2.8		10 Hz to 1 MHz from the output frequency
Phase Noise 1 kHz 10 kHz 100 kHz 1 MHz 20 MHz	-	-129 -141 -146 -153 -157	-	dBc/Hz	25°C \pm 2°C at 106.25 MHz
Storage Temperature Range	-55	-	+125	°C	

Notes: Specifications with Pad 1 E/D open circuit

¹ Place an appropriate power supply bypass capacitor next to device for correct operation

² Specified by part number



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Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V_{CC}	Frequency in MHz	Optional T&R Packaging code
PE55	45	F or G	E	W	- 100.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	W = 2.5V $\pm 10\%$	13.5 - 220 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)

* Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

Device Marking

PFFF.F P t • YMDxxx	P = Pletronics FFF.F P = Frequency in MHz, P for PECL t = Version F or G YMD = Date Code, All other marking is internal code
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Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

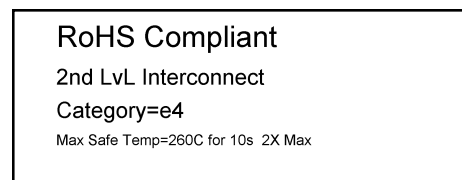
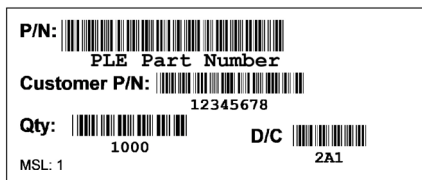
Code	3	4	5	6	7	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2023	2024	2025	2026	2027	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

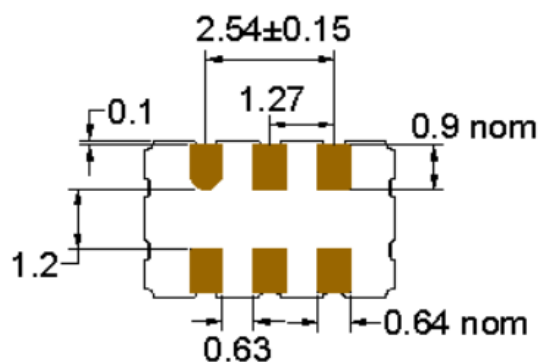
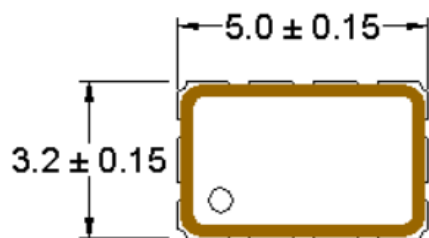
RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial



Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.056 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
Second Level Interconnect code: e4

Mechanical Dimensions



Dimensions in mm

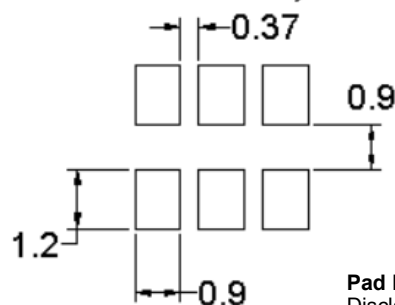
Pad Connections

Pad	Function
1	Enable/Disable
2	NC
3	Ground
4	Output
5	Output N
6	Vcc

ENABLE/DISABLE

Pad 1	Outputs
V _{IN} /Open	Active
V _{IL} /Gnd	Disabled/Tristate

Solder Pad Layout



Pad Layout

Disclaimer: Recommended layout shown.
Adjust layout as needed for individual
process requirements.

Contacts (pads): Gold (0.3 to 1.0 μ m) over Nickel (1.27 to 8.89 μ m)

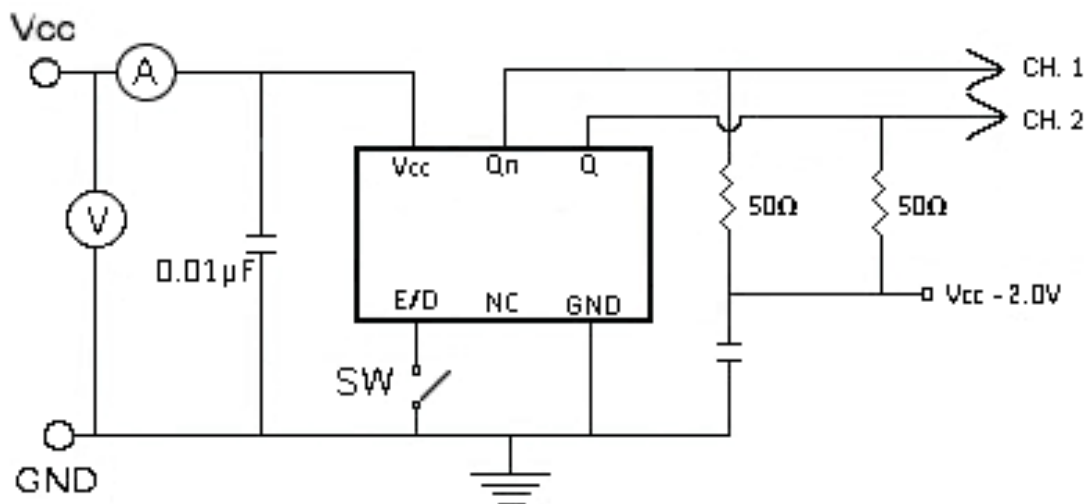
For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

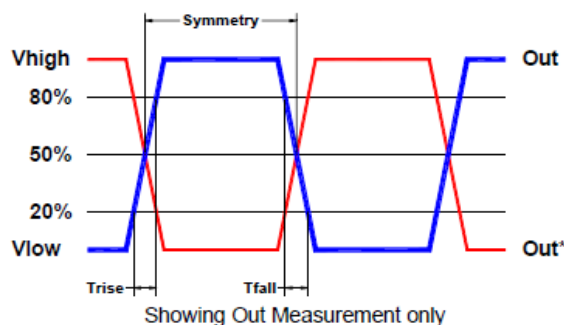


PLETRONICS PE55F/G Series 2.5V PEGL Clock Oscillator

Electrical Test /Load Circuit



Test Waveform



Environmental / ESD Ratings

Reliability: Environmental

ESD Rating

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

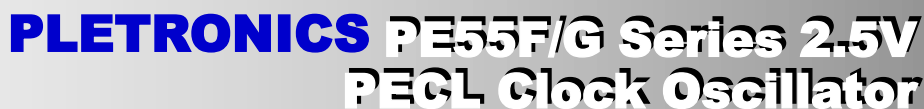
Model	Min. Voltage	Condition
Human Body Model	2000V	EIAJ ED-4701/300 Ref test method
Machine Model	200V	EIAJ ED-4701/300 Test method 304

Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +5.0V
V _i Input Voltage	-0.5V to V _{CC} + 0.5V
V _o Output Voltage	-0.5 to V _{CC} + 0.5V

Thermal Characteristics:

The maximum die or junction temperature is 150°C





PLETRONICS PE55F/G Series 2.5V PEGL Clock Oscillator

Important Notice

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