







PE55F/G 5.0 x 3.2 x 1.3 mm LCC Ceramic Package

Features

- Quartz crystal controlled Precision Square Wave Oscillator
- PECL Differential Output
- 'F' series is Fundamental; 'G' series is 3rd OT
- Enable/Disable Function on pad 1
- Low Jitter
- 3.3V nominal Supply Voltage

Applications

Driving A/Ds, D/As, FPGAs Fibre Channel Ethernet, GbE, SynchE Medical Storage Area Networking COTS Telecom PON

Electrical Characteristics					
Parameter	Min	Тур	Max	Unit	Condition
Frequency Range ² Fo	13.5 35		110 220	MHz	'F' Series 'G' Series
Frequency Stability ² ± 20 = 20 *, ± 25 = 44 , ± 50 = 45	±20	-	±50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures. *limited frequencies, see page 2
Operating Temperature Range ²	-10 -20 -40	-	+70 +70 +85	°C	Standard range Extended range C option Extended range E option
Supply Voltage 1, 2 V _{CC}	2.97	3.3	3.63	V	
Supply Current I _{CC} 'F' Series	-	33 34	44 48	mA	< 80 MHz ≥ 80 MHz
Supply Current I _{CC} 'G' Series	- - -	33 34 35 37	44 48 50 54	mA	< 90 MHz ≥ 90MHz ~ 125 MHz ≥ 125 MHz ~ 160 MHz ≥160 MHz
Output Waveform		Р	ECL		
Output High Level V _{OH}	2.275	2.35	2.42	V	Referenced to Ground
Output Low Level V _{OL}	1.49	1.6	1.68	V	Referenced to Ground
Output T _{RISE} and T _{FALL}	-	0.2	0.4	ns	Vth is 20% and 80% levels of output swing
Start Up Time	-	-	2	ms	Time for output to reach specified frequency
Duty Cycle	45	1	55	%	At output crossing point
V _{DISABLE} VIL	-	-	0.3Vcc	V	Referenced to ground
V _{ENABLE} VIH	0.7Vcc	-		V	Referenced to ground
Enable Time	-	-	2	ms	Time for output to reach a logic high state
Disable Time	-	ı	200	ns	Time for output to reach a high Z state
Enable/Disable Internal Pull-up	-	39	-	kΩ	Pin 1 open or High
Output Leakage $V_{OUT} = V_{CC}$ $V_{OUT} = 0V$	- -10	-	+10 -	μА	Pad 1 low, device disabled
Standby Current	-	-	10	μΑ	
Jitter	-	-	0.6	ps rms	12 kHz to 20 MHz from the output frequency
	-	-	2.8	'	10 Hz to 1 MHz from the output frequency
Phase Noise 1 kHz 10 kHz 100 kHz 1 MHz 20 MHz	-	-129 -141 -146 -153 -157	-	dBc/Hz	25°C ± 2°C at 106.25 MHz
Storage Temperature Range	-55	ı	+125	°C	

Notes: Specifications with Pad 1 E/D open circuit

¹ Place an appropriate power supply bypass capacitor next to device for correct operation

² Specified by part number



Part No	Part Number											
Series Model			Operating Temperature Range	Supply Voltage V _{cc}	Frequency in MHz	Optional T&R Packaging code						
PE55	55 45 F or G E		V	- 100.0M	-XX							
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	V = 3.3V ± 10%	13.5 - 220 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)						

^{*} Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

Device Marking

PFFF.FPt

YMDxxx

P = Pletronics

FFF.FP = Frequency in MHz, P for PECL

t = Version F or G

YMD = Date Code, All other marking is internal code

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	3	4		5	6		7	Code	Α		В	С	D	Е	F	(G	Н	J	K	L	М
Year	2023	202	4 2	025	2026	5 2	2027	Month	ı JA	N F	EB	MAR	APR	MAY	JUN	1 JI	UL	AUG	SEP	OCT	NOV	DEC
Code	1	2	3	4		5	6	7	8	9	Α	В	С	D	Е	F		2				
Oode	•		٠			<u> </u>	ŭ	'	Ū	,	_ ^		Ŭ		_	•		•				
Day	1	2	3	4		5	6	7	8	9	10	11	12	13	14	15	1	6				
Code	Н	J	K	_		VI	N	Р	R	Т	U	٧	w	Х	Υ	Z						
Day	17	18	19	2	0 :	21	22	23	24	25	26	27	28	29	30	31						

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

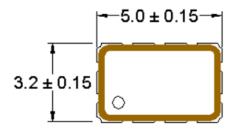
Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.056 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

Second Level Interconnect code: e4



Mechanical Dimensions



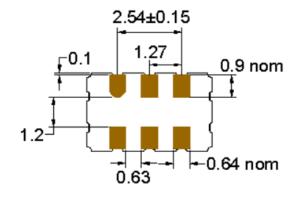


Pad Connections

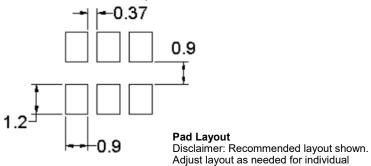
Pad	Function
1	Enable/Disable
2	NC
3	Ground
4	Output
5	Output N
6	Vcc

ENABLE/DISABLE						
Pad 1	Outputs					
Vɪн/Open	Active					
VIL/Gnd	Disabled/Tristate					

process requirements.



Solder Pad Layout



Dimensions in mm

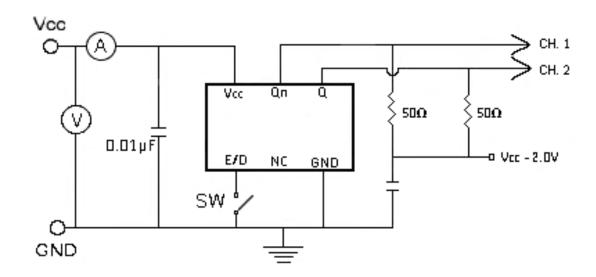
Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

For Optimum Jitter Performance, Pletronics recommends:

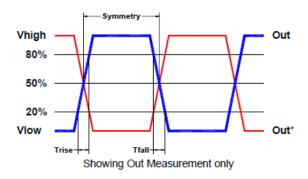
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



Electrical Test /Load Circuit



Test Waveform



Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

Thermal Characteristics:

The maximum die or junction temperature is 150°C

ESD Rating

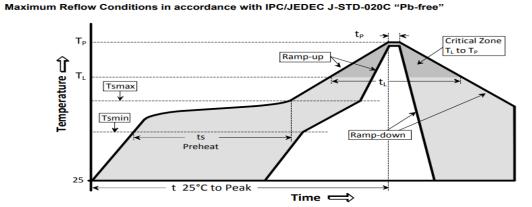
Model	Min. Voltage	Condition			
Human Body Model	2000V	EIAJ ED-4701/300 Ref test method			
Machine Model	200V	EIAJ ED-4701/300 Test method 304			

Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5 to V _{CC} + 0.5V



Reflow Cycle

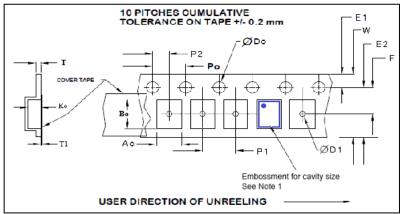


The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	(Ts _{max} to T _P)	3°C / second max	°C/s
Ramp down Rate	T _{cool}	6°C / second max	°C/s
Time 25°C to Peak Temperature	T _{to-peak}	8 minutes max	min
Preheat			
Temperature min	Ts _{min}	150	°C
Temperature max	Ts _{max}	200	°C
Time Ts _{min} to Ts _{max}	ts	60 – 180	sec
Soldering above liquidus			
Temperature liquidus	TL	217	°C
Time above liquidus	t _L	60 – 150	sec
Peak temperature	•	•	
Peak Temperature	Тр	260	°C
Time within 5°C of peak temperature	tp	20 – 40	sec

Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm (or 16mm) tape, 8mm pitch.



A B	D
<u> </u>	

Tape Variable Dimensions Table 2										
Tape Size										
12mm	10.25	5.5 ±0.05	8.0 ±0.1	12.2	3.6±0.1	5.4±0.1	1.4±0.1			
16mm	14.25	7.5 ±0.05	8.0 ± 0.1	16.3	3.6±0.1	5.4±0.1	1.4±0.1			

	Ree	Table 3	3			
		A	В		С	D
Reel Size	Inches	mm	Inches mm		mm	mm
					13.0	Tape size +0.4
7	7 7.0 180		2.50	60	+0.5 -0.2	+2.0 -0.0

Dimensions in mm Drawing Not to scale Note 1: Embossed cavity to conform to EIA- 481-B

Tape Constant Dimensions Table 1							
Tape Size	Do	D1	E1	Ро	P2	Т	T1
Size)	min				max	max
12mm	1.5 +0.1 -0.0	1.5	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.3	0.1
16mm		1.5			2.0 ±0.1		



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