



PLETRONICS SM44J Series 1.8V CMOS Clock Oscillator



SM44JX
3.2 x 2.5 x 0.95 mm
LCC Ceramic Package

Features

- Quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function includes low standby power
- Low Jitter
- 1.8V nominal Supply Voltage
- 1.25-135 MHz Frequency Range

Applications

Driving A/Ds, D/As, FPGAs
Digital Video
Ethernet, GbE
Medical
Storage Area Networking
COTS
Broad Band Access
SONET/ SDH/ DWDM
Base Stations/ Picocell
Test & Measurement

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition	
Frequency Range ²	1.25	-	135	MHz	Consult factory for other options	
Frequency Stability ² ± 20 = 20* , ± 25 = 44 , ± 50 = 45	±20	-	±50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures. *limited frequencies, see page 3	
Operating Temperature Range ²	-10 -20 -40 -40	- - - -	+70 +70 +85 +105	°C	Standard range Extended range C option Extended range E option Extended range G option	
Supply Voltage ^{1,2} (V _{CC})	1.62	1.8	1.98	V	1.8V ± 10%	
Output Waveform	CMOS					
Duty Cycle	45	-	55	%	At 50% V _{CC}	
Output V _{HIGH} (VOH)	V _{CC} - 0.4	-	-	V		
Output V _{LOW} (VOL)	-	-	0.4	V		
Output T _{RISE} and T _{FALL}	-	1.5	6	ns	C _{LOAD} = 15 pF 10% to 90% of V _{CC} See Load Circuit	
Startup Time	-	-	10	ms	Time for output to reach specified frequency	
V _{DISABLE} (VIL)	-	-	0.3V _{CC}	V		
V _{ENABLE} (VIH)	0.7V _{CC}	-				
Output Enable Time	-	-	100	ns	Time for output to reach a logic state	
Output Disable Time	-	-	200	ns	Time for output to reach a high Z state	
Enable/Disable Internal Pull-up	30	70	150	KΩ	To V _{CC} , Pad 1 open or ≥0.7V _{CC}	
Standby Current (Pad 1 low)	-	-	10 20	μA	-40 to +85°C -40 to +105°C	Output disabled to tristate
Output leakage current	- -10	-	+10 -	μA	Out = V _{CC} Out = Gnd	Pad 1 low
rms Phase Jitter	-	0.1	1.0	ps	F _o ≥ 40MHz; 12kHz ~ 20MHz	
Phase Noise	10 Hz 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 10 MHz	- - - - - - -	-78 -107 -132 -144 -151 -155 -158	- - - - - - -	dBc/Hz	25°C ± 2°C at 100 MHz
Storage Temperature Range	-55	-	+125	°C		

Notes: Specifications with Pad 1 E/D open circuit

¹ Place an appropriate power supply bypass capacitor next to V_{CC} for best performance

² Specified by part number



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Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Supply Current I_{CC}		0.4	0.8		3 MHz
		0.7	1.4		5 MHz
		0.7	1.4		10 MHz
	-	0.8	1.6		20 MHz
	-	2.5	5.0	mA	50 MHz
	-	2.5	5.0		65 MHz
	-	3.5	7.0		85 MHz
		4.0	7.5		100 MHz
		4.5	8.5		133 MHz
					no load

Specifications with Pad 1 E/D circuit open



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Part Number						
Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V _{CC}	Frequency in MHz	Optional T&R Packaging code
SM44	45	J	E	X	-125.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C G = -40 to +105°C**	X = 1.8V ± 10%	1.25 - 170	T250 = 250 per Reel T500 = 500 per Reel T3K = 3000 per Reel (Std)

* Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.
** ± 50ppm

Device Marking

PFF.FF M • YMDxx	PFF.FF M • YMxx
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PLE or P = Pletronics
FF.FF = Frequency in MHz
YMD or YM = Date Code, All other marking is internal code

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	3	4	5	6	7	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2023	2024	2025	2026	2027	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

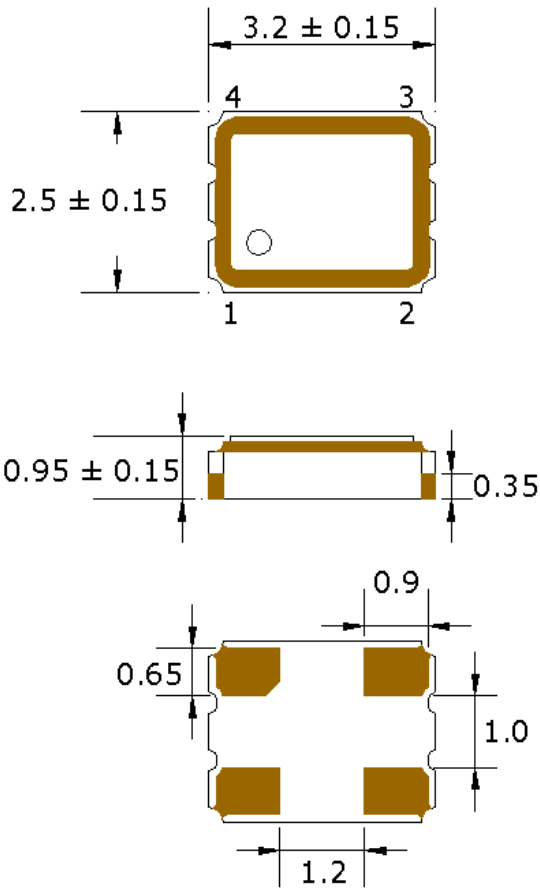
P/N:
PLE Part Number
Customer P/N:
12345678
Qty:
3000
D/C
2A1
MSL: 1

RoHS Compliant
2nd Lvl Interconnect
Category=e4
Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.024 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
Second Level Interconnect code: e4

Mechanical Dimensions

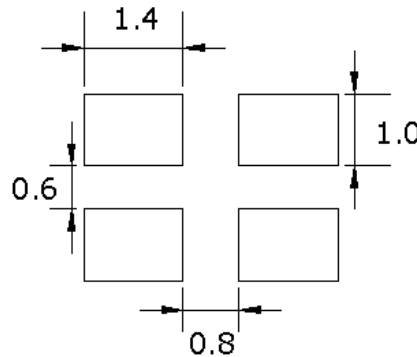


Pad Connections

Pad	Function
1	Enable/Disable
2	Ground
3	Output
4	Vcc

ENABLE/DISABLE

Pad 1	Output
V _{IH} /Open	Active
V _{IL} /Gnd	Disabled/Tristate



Pad Layout

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

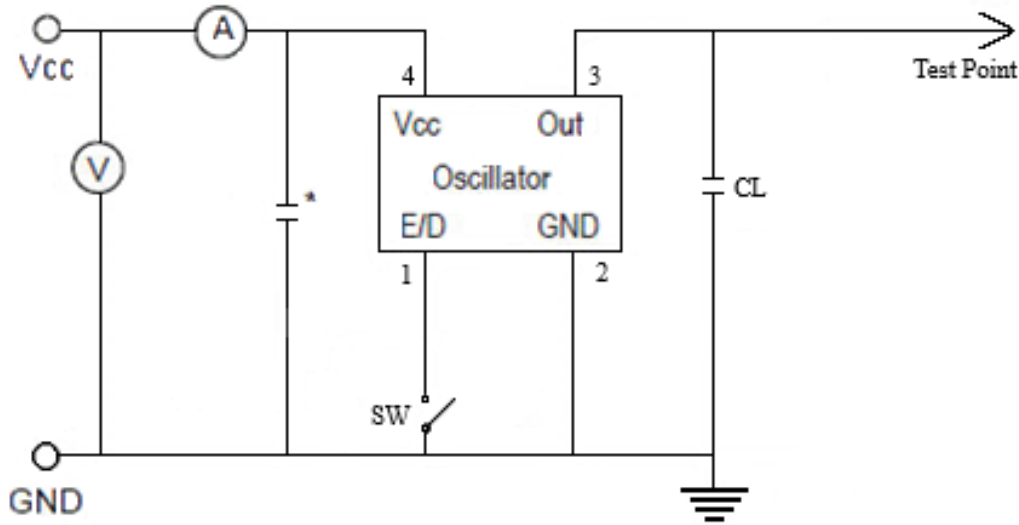
Dimensions in mm

Contacts (pads): Gold (0.3 to 1.0 μm) over Nickel (1.27 to 8.89 μm)

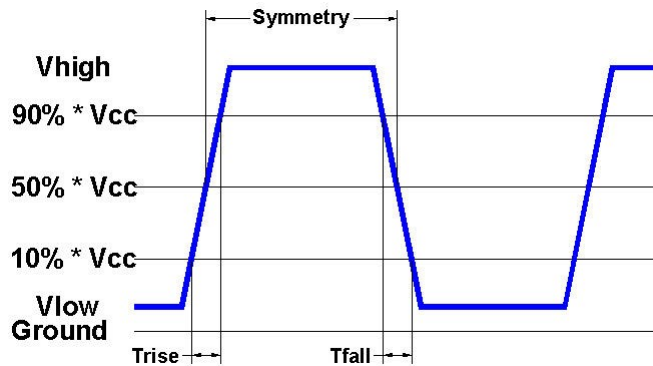
For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

Electrical Test / Load Circuit



Notes:
 CL: Includes the input capacitance of oscilloscope
 * 0.01 μ F external by-pass filter is recommended



Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Machine Model	200V	JESD22-A115

Absolute Maximum Ratings

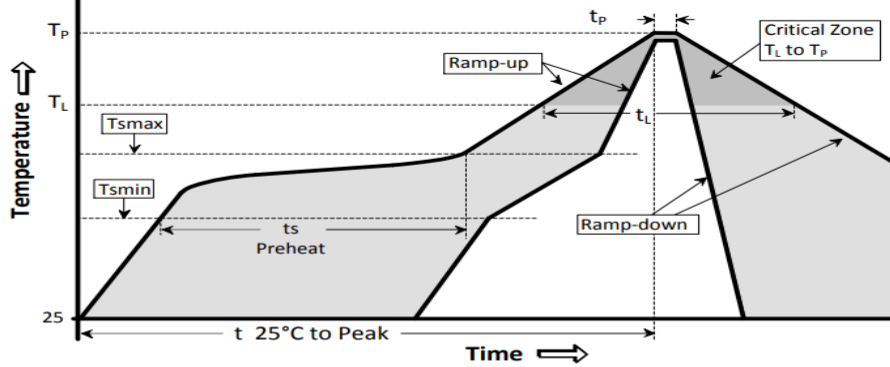
Parameter	Unit
V _{CC} Supply Voltage	-0.3V to +4.0V
V _i Input Voltage	-0.3V to V _{CC} + 0.3V
V _o Output Voltage	-0.3V to V _{CC} + 0.3V

Thermal Characteristics:

The maximum die or junction temperature is 150°C

Reflow Cycle

Maximum Reflow Conditions in accordance with IPC/JEDEC J-STD-020C "Pb-free"

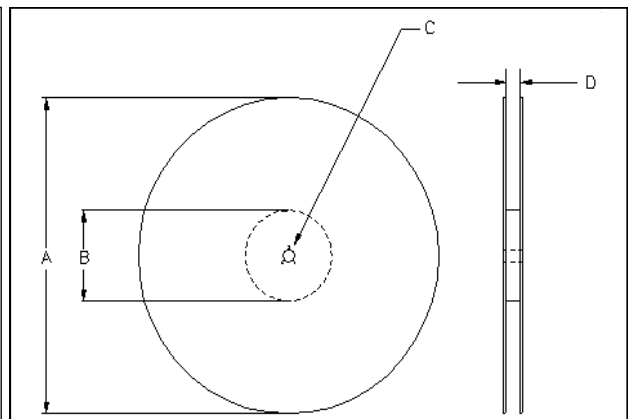
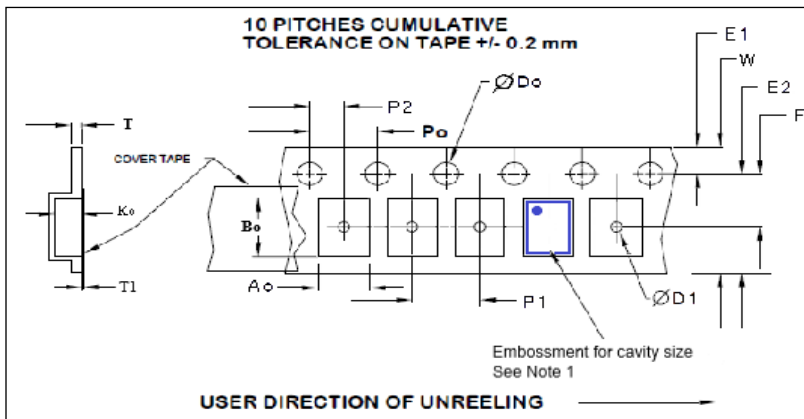


The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	($T_{S_{max}}$ to T_P)	3°C / second max	°C / s
Ramp down Rate	T_{cool}	6°C / second max	°C / s
Time 25°C to Peak Temperature	$T_{to-peak}$	8 minutes max	min
Preheat			
Temperature min	$T_{S_{min}}$	150	°C
Temperature max	$T_{S_{max}}$	200	°C
Time $T_{S_{min}}$ to $T_{S_{max}}$	t_s	60 – 180	sec
Soldering above liquidus			
Temperature liquidus	T_L	217	°C
Time above liquidus	t_L	60 – 150	sec
Peak temperature			
Peak Temperature	T_P	260	°C
Time within 5°C of peak temperature	t_p	20 – 40	sec

Tape and Reel

Tape and Reel available for quantities of 250 to 3000 per reel, cut tape for < 250. 8mm tape, 4mm pitch.



Tape Size	E2 typ	F	P1	W max	Ao	Bo	Ko
8mm	6.25	3.5 ±0.05	4.0 ±0.1	8.2	2.7 ± 0.1	3.4 ± 0.1	1.4 ± 0.1

Reel Size	A		B		C	D
	Inches	mm	Inches	mm	mm	mm
7	7.0	180	2.50	60	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0

Dimensions in mm Drawing Not to scale

Note 1: Embossed cavity to conform to EIA-481-B

Tape Size	Do	D1 min	E1	Po	P2	T max	T1 max
8mm	1.5 +0.1 -0.0	1.0	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.3	0.1



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Contacting Pletronics Inc.

Pletronics, Inc.
19013 36th Ave. West
Lynnwood, WA 98036-5761
U.S.A.

Tel: 425.776.1880
Fax: 425.776.2760
email: ple-sales@pletronics.com
URL: www.pletronics.com