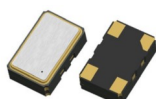




PLETRONICS OeD4304-10.0M OeXO® Oscillator



OeD4
3.2 x 5.0 x 1.85 mm
LCC Ceramic Package

Features

- Pletronics' OeXO® Series Ovenized equivalent Temperature Compensated Crystal Oscillator
- Low Power / Fast Warm Up
- CMOS Output
- 3.3V nominal Supply Voltage
- 10.0 MHz nominal frequency

Applications

SONET / SDH / DWDM
Test & Measurement
Telecom Transmission & Switching Equipment
Base Stations / Picocell
Wireless Communication Equipment

Electrical Characteristics for CMOS

Parameter	Min	Typ	Max	Unit	Condition
Frequency	-	10.0	-	MHz	
Frequency Stability vs. Temperature	-0.25	-	+0.25	ppm	Over -40°C to +85°C at fixed V _{CC} + load (reference to midpoint min/max frequency)
Frequency Initial Calibration		-	±2.0	ppm	
Operating Temperature Range	-40	-	+85	°C	
Supply Voltage ¹ V _{CC}	-	3.3	-	Volts	± 5%
Supply Current I _{CC}	-	3.0	-	mA	Load: 10pF, V _{CC} ± 5%
Frequency Stability vs. Supply	-	-	±0.2	ppm	Load: 10 pF, V _{CC} ± 5%
Frequency Stability vs. Load	-	-	±0.2	ppm	Load: 10 pF ± 10%
V _{control} Range	0.5	-	2.5	V	1.50 V nominal for V _{CC}
Frequency Pullability	±8.0	-	-	ppm	Positive Slope
Output Waveform	CMOS				
Duty Cycle	40	50	60	%	Load: 10 pF V _{th} : T _R and T _F 10% and 90% of amplitude V _{th} : D.C. 50% of amplitude
Output V _{HIGH}	90	-	-	%V _{dd}	
Output V _{LOW}	-	-	10	%V _{dd}	
Output T _{RISE} and T _{FALL}	-	-	6.5	nS	
Startup Time	-	-	10.0	mS	Within ± 2.0 ppm of final frequency
Long Term Stability (Aging)	-	-	±1.0	ppm	Per year at 25°C ± 2°C
Phase Noise	100 Hz 1 kHz 10 kHz 100 kHz	-120 -134 -144 -145	-	dBc/Hz	25°C ± 2°C at 20.0 MHz
Jitter	-	0.6	-	pS	Frequency offset from carrier 12 kHz to 20 MHz, Typical performance at 20.0 MHz
Storage Temperature Range	-55	-	+90	°C	

Note: ¹ Place a 10nF power supply bypass capacitor next to device for correct operation



Device Marking

Pff.fM • YMDxxx	ff.fM	= Frequency in MHz	Specifications such as part number, frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.
	YMD	= Date code (see table below)	
	P	= Pletronics	
	x	= Internal Factory Codes	

Code	1	2	3	4	5	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2021	2022	2023	2024	2025	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31

Package Labeling

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 16mm tape, 8mm pitch.

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

P/N:	
	OED4304-10.0M
Customer P/N:	
	12345678
Qty:	
	1000
D/C	
	11M
MSL: 1	

RoHS Compliant

2nd LvL Interconnect

Category=e4

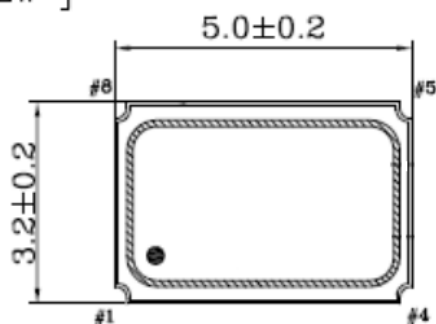
Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS 3 and WEEE 2 directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.10 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
Second Level Interconnect code: e4

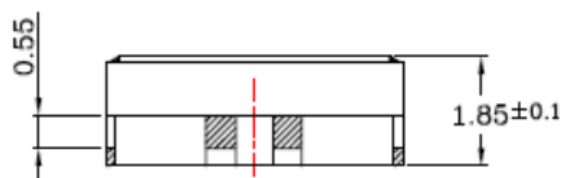
Mechanical Dimensions (mm)

[TOP VIEW]

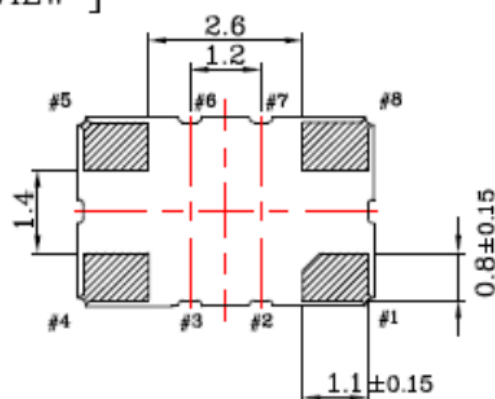


Pin 1 Mark

[SIDE VIEW]

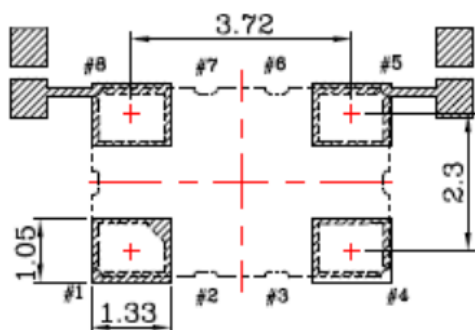


[BOTTOM VIEW]



Recommended soldering pattern

★To ensure optimal oscillator performance, place a by-pass capacitor of 0.1uF as close to the part as possible between Vdd and GND pads.



PIN#	FUNCTION
1	VCON:VC-TCXO NC:TCXO
4	GND
5	Fout
8	VDD

(Not to Scale)

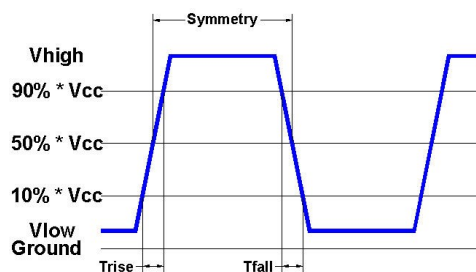
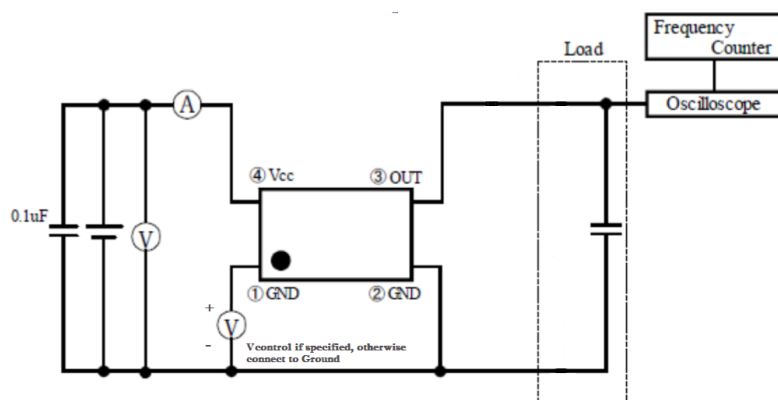
Contacts (pads): Gold 11.8 to 39.4 µinches (0.3 to 1.0 µm) over Nickel 50 to 350 µinches (1.27 to 8.89 µm)

All connection points in the designated region have solder mask cover to avoid any electrical connections (top view shown)

For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans
- Minimize air flow across the device

Electrical Test /Load Circuit



Environmental / ESD Ratings

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	JESD22-B104
Vibration	JESD22-B103
Solderability	IPC J-STD-002
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Charged Device Model	500V	JESD 22-C101
Machine Model	200V	JESD22-A115

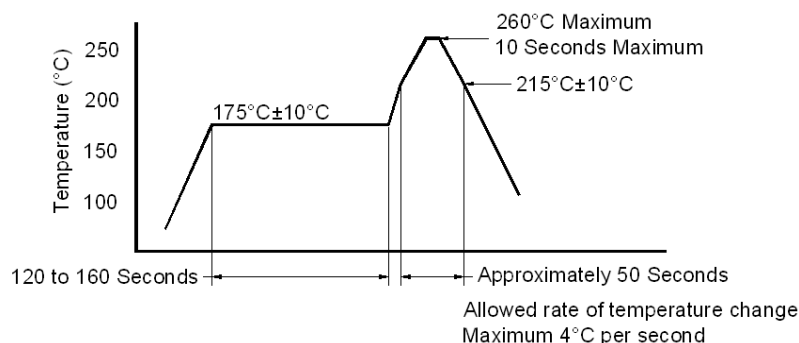
Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.6V to +6V
V _i Input Voltage	-0.6V to V _{CC} + 0.6V
I _o Output Current	-10mA to +10mA

Thermal Characteristics:

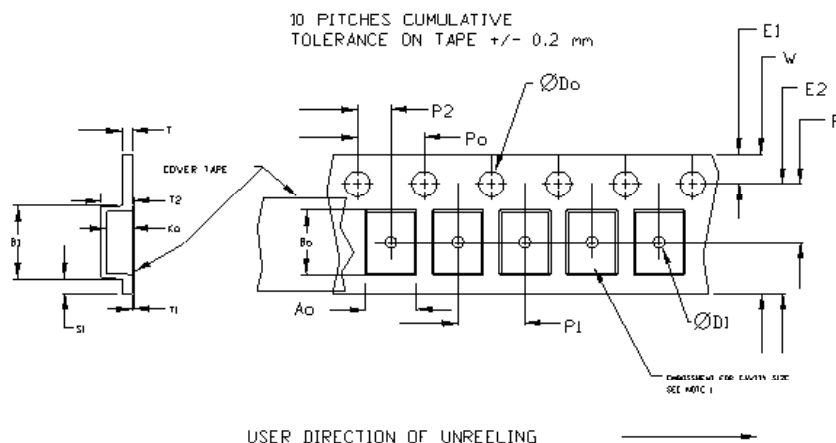
The maximum die or junction temperature is 155°C
The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.

Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel



Tape Constant Dimensions Table 1

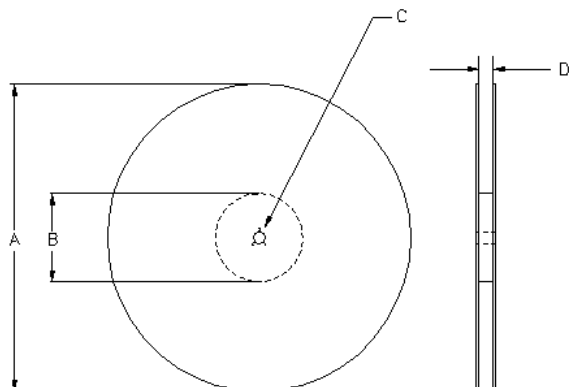
Tape Size	Do	D1 min	E1	Po	P2	S1 min	T max	T1 max
8mm	1.5 +0.1 -0.0	1.0	1.75 ±0.1	4.0 ±0.1	2.0	0.6	0.6	0.1
12mm		1.5			±0.05			
16mm		1.5			2.0			
24mm		1.5			±0.1			

Tape Variable Dimensions Table 2

Tape Size	B1 max	E2 min	F	P1	T2 max	W max	Ao, Bo & Ko
16mm	12.1	14.25	7.5 ±0.1	8.0 ±0.1	8.0	16.3	Note 1

Dimensions in mm Drawing Not to scale

Note 1: Embossed cavity to conform to EIA-481-B



Reel Dimensions (may vary) Table 3

Reel Size	A		B		C	D
	Inches	mm	Inches	mm	mm	mm
7	7.0	177.8	2.50	63.5	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0
10	10.0	254.0	4.00	101.6		
13	13.0	330.2	3.75	95.3		



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